



Vishnu Waman Thakur Charitable Trust's
VIVA Institute of Technology
 Shirgaon, Virar (East), Dist: Palghar-401305, Maharashtra
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Mumbai University Approved
 One week Short Term Training Program(STTP) on
“Hands-on Skill based Laboratories for EXTC Engineering”
 (2nd May to 7th May 2022)

Organized by
 by
 Department of Electronics and Telecommunication Engineering

PATRONS

Hon. Shri. Hitendra Thakur

President, VIVA Trust

Hon. Shri. P. D. Kodolikar

Chairman, Managing Committee, VIVA Trust

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Secretary, VIVA Trust

CONVENOR

Mrs. Archana Ingle

H.O.D.(EXTC)

COORDINATOR

Mrs. Meena Perla

Assistant Professor

For More Details

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OBJECTIVE

The objective of the Hands-on Skill based Laboratories for EXTC Engineering training program is to learn to create tools with skill lab software's that will help to implement new ideas and solve difficult problems. Learning of Linux, Raspberry Pi, FPGA will help to apply our research to interesting and challenging real-world problems. This STTP provides comprehensive coverage of Skill based Lab and its applications while providing opportunities for Hands-on practical's.

The purpose of this STTP is to bring together researchers & PG students from academia and Engineers & Scientists from industry and R&D institutes to have discussions on various ways in which these software tools can be used to carry out research.

CONTENT OF STTP

- Introduction to Linux, FPGA Raspberry Pi and Arduino.
- Use of wireless sensors/communications with Embedded systems
- Installation of Raspbian
- Program Arduino using Arduino IDE for real life applications
- FPGA design and implementation
- Interfacing of FPGA boards
- Implementation skill of different servers on Linux
- Exploring Linux commands and Shell Scripting.

KEY TAKE AWAY

- Hands on Practice on Linux, Arduino & Raspberry Pi and FPGA board
- The participants shall get to deploy remotely controlling of rpi, interface sensor, relay buzzer motor.
- After the completion of this course the participants can apply the Linux commands using programming skill to deploy different servers like ftp, telnet etc.
- The participants shall learn how to implement Embedded systems with different sensors and peripherals as IoT.
- The participants shall learn how to interface LED, switches and seven segment with FPGA.

ELIGIBILITY

Post-graduate students, research scholars, faculty members from academic institutions and universities and industry professionals

EVENT DETAILS

Date: 2nd to 7th May 2022

Time : from 10.00 am to 4.00 pm

REGISTRATION

Research Scholar/ Students / Faculty / Industry Person can register with no registration fees.

Last date of online Registration 29th April 2022.

Confirmation: 30th April 2022.

REGISTRATION LINK

<https://forms.gle/3f1Ax12cAL7vtK4u6>