Department of Electronics and Telecommunication Engineering

Topic Name:	**Embedded Systems & VLSI Design**
Name of the resource person:	**Prof. Pratik Parsewar**
Designation:	**Assistant Professor**
Organization/Institution:	**Viva Institute of Technology**
Date:	**26th December to 30th December 2017**
Time:	9.00 AM TO 4.30 PM
Number of Students:	78

Programme Summary:

The objective of the Embedded Systems & VLSI Design course was to address the research, development and design problems and advance their solutions in VLSI circuits for embedded system and widespread computing applications. This course provides comprehensive coverage of VLSI Design and its hardware description languages while providing opportunities for Hands-on practical's on FPGA Board & Raspberry Pi.

This program dealt with the basic understanding, designing and simulation of the VHDL and Eagle software which provided practical exposure to the participants. The course aimed at equipping students with skills and knowledge in order to create a better society by guiding, training and motivating the students to take up research projects. This 5 days Bridge course was conducted by **Prof. Pratik Parsewar** from **26th December to 30th December 2017** in Electronics and Telecommunication Engineering Department, VIVA Institute of Technology.