AC –
Item No. –

As Per NEP 2020

University of Mumbai

Syllabus for Major Vertical – 1, 4, 5 & 6		
Name of the Programme – B.E. (Electronics and Telecommunication)		
Faulty of Engineering		
Board of Studies in Electronics and Telecommunication Engineering		
U.G. Second Year Programme	Exit Degree	U.G. Diploma in Engineering- Electronics and Telecommunication.
Semester		III & IV
From the Academic Year		2025-26

University of Mumbai

(As per NEP 2020)

Sr. No.	Heading	Particulars
1	**Title of program** **O:**_____	**B.E. (Electronics and Telecommunication Engineering)**
2	**Exit Degree**	**U.G. Diploma in Engineering- Electronics and Telecommunication.**
3	**Scheme of Examination** **R:** _____	NEP 40% Internal 60% External, Semester End Examination Individual Passing in Internal and External Examination
4	**Standards of Passing** **R:** _____	**40%**
5	**Credit Structure** **R:** _____	**Attached herewith**
6	**Semesters**	**Sem. III & IV**
7	**Program Academic Level**	**5.00**
8	**Pattern**	**Semester**
9	**Status**	**New**
10	**To be implemented from Academic Year**	**2025-26**

Sd/-	**Sd/-**	**Sd/-**
Dr. Faruk Kazi	**Dr. Deven Shah**	**Prof. Shivram S. Garje**
BoS-Chairman- Electronics and Telecommunication Engineering	**Associate Dean**	**Dean**
Faculty of Technology	**Faculty of Science & Technology**	**Faculty of Science & Technology**

Preamble

To meet the challenge of ensuring excellence and NEP 2020 policy in engineering education, the issue of quality needs to be addressed, debated, and taken forward systematically. Accreditation is the principal means of quality assurance in higher education. The major emphasis of the accreditation process is to measure the outcomes of the program that is being accredited. In line with this, the Faculty of Science and Technology (in particular Engineering) of the University of Mumbai has taken the lead in incorporating the philosophy of NEP 2020 education in the process of curriculum development.

The second-year engineering course is a core training program to impart scientific and logical thinking training to learners in general, with a choice of course selection from the program core course, multidisciplinary minor, and vocational skill-enhanced course. Simultaneously, the objectives of NEP 2020 demand nurturing the core program and skills required for the Electronics and Telecommunication Engineering Branch of the learner. Keeping this in view, a pool of courses is offered in Core Courses covering fundamentals required to understand core and modern engineering practices and emerging trends in technology. Considering the shift in pedagogy and the convenience of a stress-free learning process, a choice-based subject pool is offered in the coursework under the heads of Electronics and Telecommunication Engineering for open electives and multidisciplinary minor courses in the third and fourth semesters. Essentially, to give a glimpse of trends in the industry under vocational and enhanced skill practices, the pool is offered to nurture and develop creative skills in contemporary industrial practices. Criteria met in the structure is the opportunity for learners to choose the course of their interest in all disciplines.

The Program Core Course Covers Electronics and Telecommunication engineering core courses. Also, OE and MDM where a pool of subjects are given for selection. Considering the present scenario, diverse choices need to be made available to fulfill the expectation of a learner to aspire for a career in the field of current trends of Technology and interdisciplinary research. Ability enhancement can be achieved in Undergraduate training by giving an objective viewpoint to the learning process and transitioning a learner from a rote learner to a creative professional. for the purpose Design Thinking is introduced in the First Semester to orient a journey learner to become a skilled professional. Considering the NEP-2020 structure of award of Certificate & Diploma at multiple exit-point pools of Vocational skills is arranged for giving exposure to the current Industry practices.

The faculty resolved that course objectives and course outcomes are to be clearly defined for every course so that all faculty members in affiliated higher education institutes understand the depth and approach of the course to be taught, which will enhance the learner's learning process. NEP 2020 grading system enables a much-required shift in focus from teacher-centric to continuous-based learner-centric education since the workload estimated is based on the investment of time in learning and not in teaching. It also focuses on continuous evaluation, which will enhance the quality of education. Credit assignment for courses is based on a 15-week teaching-learning process for NEP 2020, however, the content of courses is to be taught in 12-13 weeks, and the remaining 2-3 weeks are to be utilized for revision, tutorial, guest lectures, coverage of content beyond the syllabus, etc.

There was a concern that in the present system, the second-year syllabus must not be heavily loaded to the learner and it is of utmost importance that the learner entering into the second year of an engineering course should feel at ease by lowering the burden of syllabus and credits. This is necessary for a learner to get accustomed to the new environment of a college and to create a bond between the teacher and the learner. The present curriculum will be implemented for the Second Year of Engineering from the academic year 2025-26. Subsequently, this system will be carried forward for Third Year and Final Year Engineering in the academic years 2026-27, and 2027-28, respectively.

Sd/-	**Sd/-**	**Sd/-**
Dr. Faruk Kazi	**Dr. Deven Shah**	**Prof. Shivram S. Garje**
BoS-Chairman-	**Associate Dean**	**Dean**
Electronics and Telecommunication Engineering	**Faculty of Science & Technology**	**Faculty of Science & Technology**
Faculty of Technology		

Under Graduate Diploma in Engineering- Electronics and Telecommunication.

Credit Structure (Sem. III & IV)

	R: _____C								
Level	**Semester**	**Major**		**Minor**	**OE**	**VSC, SEC (VSEC)**	**AEC, VEC, IKS**	**OJT, FP, CEP, CC,RP**	**Cum. Cr. / Sem.**	**Degree/ Cum. Cr.**
		Mandatory	**Electives**							
5.0	III	PCC301:3 PCC302:3 PCC303:3 PCC304:3 PCL301: 1 PCL302:1	--	--	OE:2	--	VEC: 2 HSL: 2	CEP: 2	22	**UG Diploma 45**
	R:_____D									
	IV	PCC401:3 PCC402:3 PCC403:3 PCL401:1 PCL402:1	--	MDM: 4	OE:2	VSEC:2	VEC: 2 EEM:2	--	23	
	Cum Cr.	25	--	4	4	2	*2+2+2+2*	2	45	

Exit option: Award of UG Diploma in Major and MDM with 90 credits and additional 4 credits core **one** theory subject with 3 credits and **one** lab with 1 credit from one third year from where they want to take Exit degree. Along with theory and practical course student must compulsory do internship for **one month or 160 hours** which internship is equal to 4 credits.

[Abbreviation - OE – Open Electives, VSC – Vocation Skill Course, SEC – Skill Enhancement Course, (VSEC), AEC – Ability Enhancement Course, VEC – Value Education Course, IKS – Indian Knowledge System, OJT – on Job Training, FP – Field Project, CEP – Continuing Education Program, CC – Co-Curricular, RP – Research Project]

S.E. Electronics and Telecommunication Engineering Scheme

Semesters III and IV

Program Structure for Second Year of Electronics and Telecommunication Engineering

UNIVERSITY OF MUMBAI (With Effect from 2025-2026)

SEMESTER III

Course Code	Course Description	Teaching Scheme (Contact Hours)			Credit Assigned			
		Theory	Practical	Tutorial	Theory	Tutorial	Practical	Total Credits
2303111	Mathematics for Signal Analysis	2	--	1-	2	1	--	3
2303112	Electronic Devices & Linear Circuits	3	–	--	3	--	--	3
2303113	Digital System Design	3	--	--	3	--	--	3
2303114	Network Theory and Control System	3	--	--	3	--	--	3
OEC301	Open Elective	2#	--	--	2	--	--	2
2303115	Electronic Devices & Linear Circuits Laboratory	--	2	--	--	--	1	1
2303116	Digital System Design Laboratory	--	2	--	--	--	1	1
2303611	C++ and Java Programming	--	2*+2	--	--	--	2	2
2993511	Entrepreneurship Development	--	2*+2	---	--	--	2	2
2993512	Environmental Science	--	2*+2	--	--	--	2	2
Total		**13**	**16**	**01**	**13**	**01**	**08**	**22**

* Two hours of practical class to be conducted for full class as demo/discussion.
Theory / Tutorial 1 credit for 1 hour and Practical 1 credit for 2 hours

Institute shall offer a course for Open Elective from Science/Commerce/Management stream bucket provided by the University of Mumbai.

Course Code	Course Description	Examination scheme							
		Internal Assessment Test (IAT)			End Sem. Exam Marks	End Sem. Exam Duration (Hrs)	Term Work (Tw)	Oral & Pract.	Total
		IAT-I	IAT-II	Total (IAT-I) + IAT-II)					
2303111	Mathematics for Signal Analysis	20	20	40	60	2	25	--	125
2303112	Electronic Devices & Linear Circuits	20	20	40	60	2	--	--	100
2303113	Digital System Design	20	20	40	60	2	--	--	100
2303114	Network Theory and Control System	20	20	40	60	2	--	--	100
OEC301	Open Elective	20	20	40	60	2	--	--	100
2303115	Electronic Devices & Linear Circuits Laboratory	--	--	--	--	--	25	25	50
2303116	Digital System Design Laboratory	--	--	--	--	--	25	25	50
2303611	C++ and Java Programming	--	--	--	--	--	50	25	75
2993511	Entrepreneurship Development	--	--	--	--	--	50	--	50
2993512	Environmental Science	--	--	--	--	--	50	--	50
Total		**100**	**100**	**200**	**300**	**10**	**225**	**75**	**800**

Vertical – 1
Major

Detail Syllabus

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2303111	Mathematics for Signal Analysis	2	-	1	2	-	1	3

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Total					
2303111	Mathematics for Signal Analysis	20	20	40	60	2	25	--	125

Rationale:
The goal of this course is to make the learner conversant with the basic tools of mathematics for application in Electrical, Electronics, and Telecommunication engineering. The syllabus designed will help the learner build a foundation to model Signal Analysis problems mathematically, analyze and solve the same.

Prerequisite:
Applied Mathematics-I
Applied Mathematics-II

Course Objectives:

1. To introduce the concept of Laplace Transform and its application in solving ODE.
2. To familiarize with the concept of expanding periodic functions/signals in the form of Fourier Series.
3. To introduce the concept Fourier Transform and its applications.
4. To familiarize with the concept of Z-Transform for discrete functions/signals and its applications.
5. To familiarize with the concept of random variable and probability distributions with its applications in engineering and science.
6. To introduce concepts and fundamentals of Matrix algebra for engineering problems.

Course Outcomes:
On successful completion of the course learner will be able to:

1. Understand Laplace Transform and its application in solving ordinary differential equations.
2. Apply the Fourier series to expand the given periodic function/signal.
3. Apply Fourier Transform and its properties to transform the function/signal from one domain (time) to another domain(frequency).
4. Understand and apply Z-transform to discrete functions/signals.

5. Understand and apply the concept of random variable and standard probability distributions.

6. Apply the concepts of eigenvalues and eigenvectors in engineering problems.

DETAILED SYLLABUS:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
I	**Laplace Transform**	Laplace Transform & Inverse Laplace Transforms of Standard Functions like $e^{at}, \sin(at), \cos(at), \sinh(at), \cosh(at)$ and t^n, $where\ n \geq 0$ (without proof) First Shifting theorem, Laplace Transform of derivatives and integrals (Properties without proof) Inverse Laplace transform using First Shifting Theorem and Partial fractions method. Applications of Laplace Transforms for Solutions to ODE to electrical & electronics circuit problems. (Only first order differential equations). **Self -Learning Topics:** Heaviside &Dirac Delta function, Applications of Laplace Transforms for Solutions to ODE (Higher order differential equations)	**5**	**CO1**
II	**Fourier Series**	Fourier series of periodic function with period 2π and 2l. Fourier series of even and odd functions (-l, l), Complex form of Fourier Series$(-\infty, \infty)$(No deductions on the basis of Fourier Series) **Self-learning Topics:** Parseval's Identity, Half-range Cosine/sine Series, Fourier Integral	**4**	**CO2**
III	**Fourier Transform**	Fourier transform, Fourier Transform of Heaviside Unit step Function and Dirac Delta Function. Linearity Property, Time shifting Property, Frequency Shifting Property, convolution and Modulation property, Question related to (Electrical & Extc engineering) **Self -Learning Topics:** Standard Signals-Stop, input, Delta, Exponential Signals	**5**	**CO3**
IV	**Z-Transform**	Definition and Region of Convergence, Transform of Standard Functions: $\{k^n a^k\}, \{a^k\}, \{c^k \sin(\alpha k + \beta)\}, \{c^k sinh\alpha k\}, \{c^k \cos(\alpha k + \beta)\}, \{c^k cosh\alpha k\}$. Properties of Z Transform: Change of Scale, Shifting Property, Multiplication, and Division by k, Convolution theorem. Inverse Z transform: Partial Fraction Method, Question related to (Electrical & Extc engineering) **Self-learning Topics:** Initial value theorem, Final value theorem, Inverse Using Convolution Theorem	**4**	**CO4**
V	**Random Variable & Probability Distribution**	Discrete and Continuous random variables, Probability mass and density function, Probability distribution for random variables, Expectation, Variance, Poisson and Normal distribution (No question on finding the mean and variance)	**4**	**CO5**

		Self-Learning Topics: Binomial Distribution, Moment generating function		
VI	**Linear Algebra (Theory of Matrices**)	Characteristic Equation, Eigenvalues and Eigenvectors, and properties of eigenvalues (without proof) Similarity of matrices, diagonalizable and non-diagonalizable matrices **Self-learning Topics**: Cayley-Hamilton Theorem and its usage in reduction of higher degree polynomials Derogatory and non-derogatory matrices, Function of a square Matrix	**4**	**CO6**

Note:

- **Tutorial shall be conducted batch wise.**
- **No Questions to be asked from Self-Learning Topics.**

Text / Reference Books:

1. Higher Engineering Mathematics, Dr. B. S. Grewal, Khanna Publication.
2. Advanced Engineering Mathematics, Erwin Kreyszig, Wiley Eastern Limited.
3. Advanced Engineering Mathematics, R. K. Jain and S. R. K. Iyengar, Narosa publication
4. Signals and Systems, A Nagoor Nani, Tata McGraw Hill.
5. Probability, Statistics and Random Processes, T. Veerarajan, Mc. Graw Hill education.

Online References:

Sr. No.	Website Name
1.	https://nptel.ac.in/courses/111/106/111106139/
2.	https://www.youtube.com/watch?v=2CP3m3EgL1Q
3.	https://www.youtube.com/watch?v=Hw8KHNgRaOE

Term Work:

General Instructions:

1. Batch wise tutorials are to be conducted. The number of students per batch should be as per the university pattern for practical.

2. Students must be encouraged to write at least 6 class tutorials on entire syllabus.

3. A group of 4-6 students should be assigned a self-learning topic. Students should prepare a presentation/problem solving of 10-15 minutes. This should be considered as mini project in Engineering mathematics. This project should be graded for 10 marks depending on the performance of the students.

The distribution of Term Work marks will be as follows –

1.	Attendance (Theory and Tutorial)	05 marks
2.	Class Tutorials on entire syllabus	10 marks
3.	Mini project	10 marks

Assessment:

Internal Assessment (IA) Test:

Assessment consists of two class tests of 20 marks each. The first class test (Internal Assessment I) is to be conducted when approx. 40% syllabus is completed and second class test (Internal Assessment II) when additional 35% syllabus is completed. Duration of each test shall be one hour.

End Semester Examination:

1. Question paper will comprise of total 06 questions, each carrying 15 marks.
2. Total 04 questions need to be solved.
3. Question No: 01 will be compulsory and should cover maximum content of the entire syllabus.
4. Remaining questions will be randomly selected from all the modules as per the weightage of each module (which is proportional to number of respective lecture hours mentioned in the syllabus).

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2303112	Electronic Devices and Linear Circuits	3	-	-	3	-	-	3

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Total					
2303112	Electronic Devices and Linear Circuits	20	20	40	60	2	--	--	100

Prerequisite:

1. Engineering Physics-I
2. Engineering Physics-II
3. Basic Electrical Engineering

Course Objectives:

1. To explain functionality of different electronic devices.
2. To perform DC and AC analysis of small signal amplifier circuits.
3. To explain working of differential amplifiers and its applications in operational amplifiers.
4. To understand the concept working principles of Linear Integrated Circuits.
5. To Perform analysis of Linear Integrated Circuits.
6. To design circuits and systems for particular applications using Linear Integrated Circuits.

Course Outcomes:

After successful completion of the course student will be able to

1. Explain working of various electronics devices.
2. Derive expressions for performance parameters of BJT and MOSFET circuits.
3. Understand the fundamentals and areas of applications for the Integrated circuits.
4. Develop the ability to design Linear and Non-Linear application of Integrated Circuits.
5. Cultivate the skill of designing Timer circuits.
6. Gain the skill to design Voltage regulator using Integrated Circuits.

DETAILED SYLLABUS:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
0	**Prerequisite**	Introduction of Diode, BJT, JFET, MOSFET	01	CO1
I	**Biasing of BJT and MOSFET**	1.1 Construction, working and characteristics of BJT (CE configuration) and E-MOSFET (CS configuration). 1.2 Concept of DC load line, Q point and regions of operations, Biasing circuits for BJT (Fixed bias & Voltage divider Bias). 1.3 DC load line and region of operation for E-MOSFET, Biasing circuits for E-MOSFET (Drain to Gate bias & voltage divider bias).	06	CO1
II	**Small Signal Analysis of BJT and MOSFET Amplifier.**	2.1 Concept of AC load line and Amplification, Small signal analysis (Zi, Zo, Av and Ai) of CE amplifier using hybrid pi model. 2.2 Small signal analysis (Zi, Zo, Av) of CS (for E-MOSFET) amplifiers. 2.3 Frequency response of amplifier, Effect of coupling bypass and parasitic capacitor on frequency response. Millers theorem.	06	CO2
III	**Introduction to Differential Amplifier and Operational Amplifier.**	3.1 E-MOSFET Differential Amplifier, Differential and common mode gain, CMRR, differential and common mode input impedance. 3.2 Block diagram of Op-Amp , Ideal and Practical characteristics of Op-Amp. Open loop and Closed loop configuration of Op-Amp.. 3.3 Inverting and Non-inverting Amplifier using Op-Amp, Summing Amplifier, Difference Amplifier.	06	CO3
IV	**Linear and Non-Linear Applications of Operational Amplifier**	4.1 Integrator & differentiator (ideal & practical), Active Filters: First and Second order active low pass, high pass. 4.2 Comparators: Inverting comparator, non-inverting comparator. Schmitt Triggers: Inverting Schmitt trigger, non-inverting Schmitt trigger. 4.3 Positive feedback, Barkhausen's criteria, Sine Wave Oscillators: RC phase shift oscillator, Wien bridge oscillator.	08	CO4

V	**Timer IC555 and its applications.**	5.1 IC 555 Timer: Block Schematic, Functional Diagram, Working of IC 555. 5.2 Design of Monostable and Astable multivibrator using IC 555. 5.3 Applications of astable and monostable multivibrator as Pulse Width Modulator and Pulse Position Modulator.	06	CO5
VI	**Voltage Regulators.**	6.1 Block diagram of regulated DC power supply. Functional block diagram, working and design of three terminal fixed voltage regulators (78XX, 79XX series). 6.2 Functional block diagram, working and design of general purpose IC 723 (HVLC and HVHC). 6.3 Design of regulator using three terminal IC LM 317.	06	CO6

Text Books:

1. Donald A. Neamen, "Electronic Circuit Analysis and Design", Tata McGraw Hill, 2nd Edition

2. D. Roy Choudhury and S. B. Jain, "Linear Integrated Circuits", New Age International Publishers, 4th Edition.

3. Ramakant A. Gaikwad, " Op-Amps and Linear Integrated Circuits", Pearson Prentice Hall, 4th Edition

References:

1. S. Salivahanan, N. Suresh Kumar, "Electronic Devices and Circuits", Tata Mc-Graw Hill, 3rd Edition
2. Boyiestad and Nashelesky, " Electronic Devices and Circuits Theory", Pearson Education,11th Edition
3. A.K. Maini, " Electronic Devices and Circuits", Wiley
4. K.R. Botkar, "Integrated Circuits", Khanna Publisher (2004)
5.David A. Bell, " Operation Amplifiers and Linear Integrated Circuits", Oxford University Press, Indian Edition.

Online References:

Sr. No.	**Website Name**
1.	NPTEL/ Swayam Course: Course: Integrated Circuits and Applications By Prof. Shaik Rafi Ahamed (IIT Guwahati) https://onlinecourses.nptel.ac.in/noc25_ee43/preview

2.	Course: ICs MOSFETs Op-Amps & Their Applications By Prof. Hardik Jeetendra Pandya (IISc Bangalore); https://swayam.gov.in/nd1_noc20_ee13/preview

Assessment:

Internal Assessment (IA) Test:

Assessment consists of two class tests of 20 marks each. The first class test (Internal Assessment I) is to be conducted when approx. 40% syllabus is completed and second class test (Internal Assessment II) when additional 35% syllabus is completed. Duration of each test shall be one hour.

End Semester Examination:

1. Question paper will comprise of total 06 questions, each carrying 15 marks.
2. Total 04 questions need to be solved.
3. Question No: 01 will be compulsory and should cover maximum content of the entire syllabus.
4. Remaining questions will be randomly selected from all the modules as per the weightage of each module (which is proportional to number of respective lecture hours mentioned in the syllabus).

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2303113	Digital System Design	3	--	--	3	--	--	3

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Total					
2303113	Digital System Design	20	20	40	60	2	--	--	100

Prerequisite:
Basic Electrical Engineering

Course Objectives:

1. To understand number system representations and their inter-conversions used in digital electronic circuits.
2. To understand the functionalities, and Characteristics of Logic Families and Minimization techniques to realise logical operations.
3. To analyze digital logic processes and to implement logical operations using various combinational logic circuits.
4. To analyze, design and implement the logical operations using different sequential logic circuits.
5. To equip students with the knowledge and skills to design, and implement various registers, counters, and programmable logic devices.
6. To get acquainted with the basics of VHDL language.

Course Outcomes:

On successful completion of the course student will be able to:

1. Apply the concepts of number systems and perform code conversions.
2. Classify logic families, Understand Digital circuits and apply minimization techniques to implement logical functions.
3. Analyze, design and implement combinational logic circuits.
4. Analyze, design and implement sequential logic circuits.
5. Analyze, design and implement digital circuits using different registers, counters, and programmable logic devices.
6. Use HDL & appropriate EDA tool for logic design and simulation using VHDL/Verilog.

DETAILED SYLLABUS:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
I	**Number Systems and Codes**	Review of Binary, Octal and Hexadecimal Number Systems, their inter-conversion, Gray code and BCD code, Binary Addition, Subtraction using 1's and 2's Complement method.	**02**	CO1
II	**Logic families and Minimization Techniques**	Classification of logic families: Unipolar and Bipolar Logic Families, Characteristics of Digital ICs, TTL and CMOS comparison. Digital logic gates, Universal gates, Realization using NAND and NOR gates, Boolean Algebra, De Morgan's Theorem. Minimization of Boolean expressions :- SOP, POS, and Karnaugh map (up to 4 variables)	**08**	CO2
III	**Combinational Logic Circuits**	Adder, Subtractor, Multiplexer, De-multiplexer, Code Converter, BCD adder, Magnitude Comparator, Parallel Adder, Implementation of Logic expressions using Multiplexers, De-multiplexers, Encoders and Decoders.	**08**	CO3
IV	**Sequential Logic Circuits**	Flip flops (FF): SR, JK, T, D, Master Slave JK flip flops, Truth table, excitation table, triggering methods, and flip flop conversions. Counters: Asynchronous and Synchronous - MOD N, UP/DOWN, Decade counter, Frequency division, Finite State Machine: Introduction to Moore and Mealy machines - Block diagram, state diagram, state tables.	**10**	CO4
V	**Shift Registers and Programmable Logic Devices**	Registers: SISO, SIPO, PISO, PIPO, Universal Shift registers, Ring counter, Johnson counter, Sequence generator. Structure of Programmable Logic Devices (PLDs), Function implementation with Programmable Logic Array (PLA) and Programmable Array Logic (PAL). Introduction to CPLD and FPGA.	**06**	
VI	**Introduction to VHDL**	VLSI Design flow (Frontend): Design entry: Schematic different modeling styles in VHDL, Data types and objects, Synthesis and Simulation, implementation of combinational and sequential logic using VHDL.	**05**	CO6

Text Books:

1. R.P. Jain, "Modern Digital Electronics", Tata McGraw Hill Publication, 4th Edition.
2. Morris Mano, Michael D. Ciletti, "Digital Design", Pearson Education, Fifth Edition (2013).
3. A. Anand Kumar, "Fundamentals of Digital Circuits", PHI, Fourth Edition (2016).

4. J. Bhaskar A Verilog HDL Primer , Third Edition, Star Galaxy publishing
5. Sameer Palnitkar “Verilog HDL, A guide to digital
6. Douglas Perry, “VHDL programming”, McGraw Hill, fourth edition.

References:

1. John F. Warkerly, “Digital Design Principles and Practices”, Pearson Education,Fifth Edition (2018).
2. Digital fundamentals by FLOYD & JAIN, Pearsons Pub
3. Charles Roth, “Digital System Design using VHDL”, Tata McGraw Hill

Assessment:

Internal Assessment (IA) Test:

Assessment consists of two class tests of 20 marks each. The first class test (Internal Assessment I) is to be conducted when approx. 40% syllabus is completed and second class test (Internal Assessment II) when additional 35% syllabus is completed. Duration of each test shall be one hour.

End Semester Examination:

1. Question paper will comprise of total 06 questions, each carrying 15 marks.

2. Total 04 questions need to be solved.

3. Question No: 01 will be compulsory and should cover maximum content of the entire syllabus.

4. Remaining questions will be randomly selected from all the modules as per the weightage of each module (which is proportional to number of respective lecture hours mentioned in the syllabus).

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2303114	Network Theory and Control System	3	-	-	3	-	-	3

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Total					
2303114	Network Theory and Control System	20	20	40	60	2	--	--	100

Prerequisite:

1. Basic Electrical Engineering
2. Engineering Mathematics II

Course Objectives:

1. To evaluate the Circuits using network theorems, study network Topology, network Functions and two port networks.
2. To analyze the Circuits in time and frequency domain.
3. To synthesize passive network by various methods.
4. To analyze fundamental concepts of mathematical modeling, time response and Frequency response.
5. To develop concepts of stability and its assessment criteria.

Course Outcomes:

After successful completion of the course student will be able to

1. Evaluate circuit using network theorems.
2. Apply the time and frequency method of analysis.
3. Analyze the network function and finding the various parameters of two port network
4. Analyze the response and determine the transfer function of Control System
5. Understand the analysis of systems in time domain and predict stability of given system using appropriate criteria.
6. Understand the analysis of systems in frequency domain and predict stability of given system using appropriate criteria.

DETAILED SYLLABUS:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
I	**Electrical Circuit Analysis**	**I.1)** Analysis of Circuits with dependent sources using generalized loop and node analysis, super mesh and super node analysis technique Network Theorems with dependent sources: Superposition, Thevenin's, Norton's and Maximum Power Transfer Theorems **(Use only DC source)**	4	CO1
II	**Time and frequency domain analysis**	**II.1)** Time domain analysis of R-L and R-C Circuits: Forced and natural response, initial and final values. Solution using first order and second order differential equation with step signals	8	CO 2
		II.2) Frequency domain analysis of R-L-C Circuits: Forced and natural response, effect of damping factor. Solution using second order equation for step signal.		
III	**Network functions and Two Port Networks**	**III.1)** Network functions for the one port and two port networks, driving point and transfer functions, Poles and Zeros of Network functions.	6	CO3
		III.2) Two Port Parameters: Open Circuits, short Circuit, Transmission and Hybrid parameters, relationship among parameters, conditions for reciprocity and symmetry		
IV	**Analysis and response of control system**	**IV.1)** Open and closed loop systems, Transfer function modeling (Electrical only), Block diagram reduction techniques and Signal flow graph.	8	CO4
		IV.2) Dynamic Response: Standard test signals, transient and steady state behavior of first and second order systems, steady state errors in feedback control systems and their types.		
V	**Stability Analysis in Time Domain**	**V.1)** Concept of stability: Routh and Hurwitz stability criterion	6	CO5
		V.2) Root locus Analysis: Root locus concept, general rules for constructing root-locus, root locus analysis of control system.		
VI	**Stability Analysis in Frequency Domain**	**VI.1)** Frequency domain specification, Relationship between time and frequency domain specification of system, stability margins	7	CO6
		VI.2) Bode Plot: Magnitude and phase plot, Method of plotting Bode plot, Stability margins and analysis using Bode plot. Concept of Polar plot		

Textbooks:

1. Franklin F Kuo, “Network Analysis and Synthesis”, Wiley Toppan, 2 nd ed. ,1966.
2. M E Van Valkenburg, “Network Analysis”, Prentice-Hall of India Pvt Ltd, New Delhi, 26th Indian Reprint, 2000.

References:

1. A. Chakrabarti, “Circuit Theory”, Dhanpat Rai & Co., Delhi, 6th Edition.
2. A. Sudhakar, Shyammohan S. Palli “Circuits and Networks”, Tata McGraw-Hill education.
3. Smarajit Ghosh “Network Theory Analysis & Synthesis”, PHI learning.
4. K.S. Suresh Kumar, “Electric Circuit Analysis” Pearson, 2013.
5. D. Roy Choudhury, “Networks and Systems” , New Age International, 1998.
6. Nagrath, M.Gopal, “Control System Engineering”, Tata McGrawHill.
7. Rangan C. S., Sarma G. R. and Mani V. S. V., "Instrumentation Devices And Systems", Tata McGraw-Hill, 2nd Ed.,2004.
8. K.Ogata, “Modern Control Engineering, Pearson Education”, IIIrd edition.

NPTEL / Swayam Course:

1. Course: Basic Electrical Circuits By Prof. Nagendra Krishnapura (IIT Madras); https://swayam.gov.in/nd1_noc20_ee64/preview.
2. Course: Control Systems By Prof. C. S. Shankar Ram (IIT Madras); https://swayam.gov.in/nd1_noc20_ee90/preview

Assessment:

Internal Assessment (IA) Test:

Assessment consists of two class tests of 20 marks each. The first class test (Internal Assessment I) is to be conducted when approx. 40% syllabus is completed and second class test (Internal Assessment II) when additional 35% syllabus is completed. Duration of each test shall be one hour.

End Semester Examination:

1. Question paper will comprise of total 06 questions, each carrying 15 marks.

2. Total 04 questions need to be solved.

3. Question No: 01 will be compulsory and should cover maximum content of the entire syllabus.

4. Remaining questions will be randomly selected from all the modules as per the weightage of each module (which is proportional to number of respective lecture hours mentioned in the syllabus).

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2303115	Electronic Devices & Linear Circuits Laboratory	--	2	--	--	1	--	1

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical/ Oral	Total
		Internal assessment			End Sem. Exam			
		Test1	Test 2	Avg. of 2 Tests				
2303115	Electronic Devices & Linear Circuits Laboratory	--	--	--	--	25	25	50

Laboratory Objectives:

1. To make students familiar with equipment and measuring instruments used to perform this laboratory course.
2. To provide hands on experience to develop laboratory setup for performing given experimental using various equipment, electronic devices and measuring instruments.
3. To develop an ability among students to gather appropriate data and analyse the same to relate theory with practical.
4. To develop trouble shooting abilities among students

Laboratory Outcomes:

After successful completion of the course student will be able to

1. Know various equipment used in this laboratory course.
2. Understand how to make use of various devices and equipment to perform laboratory work.
3. Perform given experiment by making proper connections between various components, equipment and measuring devices for this course.
4. Acquire requisite data and analyze the same for this course.
5. Evaluate various parameters of the given circuit for this course.
6. Design the circuit for a given application for this course.

Suggested List of Experiments:

Sr No	List of Experiments	Hrs.
01	To study BJT biasing Circuits.	2
02	To Study BJT as CE amplifier.	2
03	To study EMOSFET biasing circuits	2
04	To study EMOSFET as CS amplifier.	2

05	**Simulations Experiment on study of Frequency Response of CS amplifier.**	**2**
06	**Simulations Experiment on study of Differential amplifier**	**2**
07	**Design and Implementation of Adder circuits using OPAMP.**	**2**
08	**Design and Implementation of Difference Amplifier using OPAMP.**	**2**
09	**Design and analyze Integrator circuit using OPAMP.**	**2**
10	**Design and analyze Differentiator circuit using OPAMP.**	**2**
11	**Design and analyze Schmitt trigger using OPAMP.**	**2**
12	**Design and analyze RC phase shift Oscillator.**	**2**
13	**Design and analyze first order High pass and Low pass filter.**	**2**
14	**Design of Monostable Multivibrator Circuit using 555 Timer**	**2**
15	**Design of Astable Multivibrator Circuit using 555 Timer**	**2**

Assessment:

Term Work: Term Work shall consist of at least 10 to 12 practicals' based on the above list. Also, Term work Journal must include at least 2 assignments.

Term Work Marks: 25 Marks (Total marks) = 15 Marks (Experiment) + 5 Marks (Assignments) + 5 Marks (Attendance)

Practical & Oral Exam: An Oral & Practical exam will be held based on the above syllabus.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2303116	Digital System Design Laboratory	--	2	--	--	1	--	1

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical/ Oral	Total
		Internal assessment			End Sem. Exam			
		Test1	Test 2	Avg. of 2 Tests				
2303116	Digital System Design Laboratory	--	--	--	--	25	25	50

Laboratory Objectives:

1. To get familiarise with basic building blocks of Digital System Design and verify the operation of various digital ICs.
2. To understand and implement digital circuits for code conversion.
3. To train students to design and implement combinational circuits.
4. To instruct students on how to design and implement sequential circuits.
5. To understand digital logic simulation using the EDA tool.

Laboratory Outcomes:

After successful completion of the course student will be able to

1. Identify various Digital ICs and basic building blocks of digital system design
2. Design and implement combinational circuits like adder, subtractor, multiplexer, code converters etc.
3. Identify and understand the working of various types of flip flops and their interconversions.
4. Design and implement basic sequential circuits such as counters, registers etc
5. Develop and simulate VHDL architectural representations of digital systems and components using structural, behavioural, or data flow concepts

Suggested List of Experiments:

Sr No	List of Experiments	Hrs.
01	Study of characteristics of typical TTL and CMOS IC's like fan out, noise margin, propagation delay.	02
02	Implement AND, OR, NOT, EXOR, EX-NOR gates using Universal gates NAND and NOR.	02
03	Simplify the logical expressions using Boolean algebra/k-map technique and implement using logic gates.	02

04	Implement digital circuits to perform code conversions like Binary to Gray and Gray to Binary, BCD to 7 segment decoder operations.	02
05	Design and implement Encoder/ Decoder using IC.	02
06	Design and implement logic equations using Multiplexer IC.	02
07	Flip-flop conversions JK to D, JK to T and D to T FF.	02
08	Design and implementation of ripple and synchronous counters using JK and D FF and additional gates.	02
09	Design of counter using ICs like 7490/93 (ripple) and 74192/193(synchronous)	02
10	Study of Universal Shift Register using IC-74194.	02
11	Design a Ring/ Johnson's counter using IC-74194.	02
12	Implement a universal gates using VHDL/Verilog	02
13	Implement adder circuits using VHDL/Verilog	02
14	Design a Multiplexer using VHDL/Verilog	02
15	Design a 3-bit linear feedback shift register (LFSR) using VHDL/Verilog	02
16	Design a 3-bit Array Multiplier using VHDL/Verilog	02
17	Design a 2-bit Vedic Multiplier using VHDL/Verilog	02
18	Design and implementations of random sequence counter using D FF or JK FF ICs	02
19	Comparator using IC 7485 and Parity generator and checker using X-OR gate	02
20	Binary and BCD adders and Subtractor using IC 7483 and gates	02
21	Design asynchronous/synchronous MOD N counter using IC7490	02
22	Design and implement Magnitude Comparator.	02

Sr. No.	List of Assignments / Tutorials	Hrs.
01	Number Systems and Interconversions, Binary Codes.	01
02	Boolean Algebra and Minimization using K-Map.	01
03	Digital logic gates, Universal gates, Realization using NAND and NOR gates.	01
04	Design of Combinational and Sequential Logic Circuits.	01
05	PLDs and VHDL.	01

Assessment:

Term Work: Term Work shall consist of at least 10 to 12 practicals' based on the above list. Also, Term work Journal must include at least 2 assignments.

Term Work Marks: 25 Marks (Total marks) = 15 Marks (Experiment) + 5 Marks (Assignments) + 5 Marks (Attendance)

Practical & Oral Exam: An Oral & Practical exam will be held based on the above syllabus.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2304111	Neural Networks and Fuzzy Logic	2	--	1	2	--	1	3

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Total					
2304111	Neural Networks and Fuzzy Logic	20	20	40	60	2	25	--	125

Prerequisite:

1. Applied Mathematics-I
2. Applied Mathematics-II
3. Digital System Design

Course Objectives:

1. To study the basics of biological Neural Networks.
2. To understand the various terminologies in Artificial Neural networks.
3. To understand the different types of Artificial Neural Networks.
4. To study fuzzy logic and to provide knowledge of fuzzy logic to design the real-world fuzzy systems.

Course Outcomes:

After successful completion of the course student will be able to

1. Differentiate Biological system from Artificial Neuron.
2. Explain the different learning models and distinguish between the different types of supervised and unsupervised learning neural networks.
3. Evaluate the given neural network for specific input patterns and activation functions.
4. Apply the concept of competitive neural networks for clustering applications.
5. Understand the basic concept of fuzzy sets and fuzzy relations.
6. Develop innovative solutions to real-world problems using fuzzy logic

DETAILED SYLLABUS:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
I	**Introduction to Neural Networks**	Biological neurons and its working Artificial neurons, Terminologies used in Artificial Neural Networks, Artificial Neuron Model, Applications of ANN, McCulloch-Pitts Model for AND, OR and NOT Gates	4	CO1
II	**Fundamentals of Artificial Neural Networks**	Activation Functions, Neural network architectures, Supervised, Unsupervised, Reinforcement learning rules, Linear Separability, Design of AND / OR gate using linear separability, XOR problem	4	CO2
III	**Supervised Learning**	Single Layer Perceptron, Adaline Network, Multilayer Perceptron, Error Back propagation algorithm	5	CO3
IV	**Unsupervised Learning**	Neural network based on competition: Kohonen Self Organizing Maps and Applications, learning vector quantization	4	CO4
V	**Introduction to Fuzzy Logic**	Introduction to Fuzzy Sets, Fuzzy set operations, Properties of Fuzzy sets. Fuzzy Relations: Cartesian product of Relation, Fuzzy Max-Min and Max-Product Composition.	4	CO5
VI	**Fuzzy Systems**	Features of Membership functions, Fuzzification (Intuition Method), Defuzzification (Centroid and mean of Maximum), Fuzzy Inference System and its types, Designing Fuzzy logic control systems like washing machines, and train brake control.	5	CO6

Text Books:

1. S.N. Sivanandam & S.N.Deepa , “Principles Of Soft Computing “, Wiley India Pvt. Limited, 2007
2. Timothy J Ross, “Fuzzy Logic With Engineering Applications”, John Willey And Sons, West Sussex, England, 2005.
3. Jack M. Zurada, “Introduction To Artificial Neural Systems”, PWS Publishing Co., Boston, 2002.

References:

1. Kosko, B, “Neural Networks And Fuzzy Systems: A Dynamical Approach To Machine Intelligence”, Prentice hall, New Delhi, 2004.

2. S. Rajasekaran, And G. A. Vijayalakshmi Pai , “Neural Networks, Fuzzy Logic And Genetic Algorithms : Synthesis, And Applications”, Prentice Hall Of India, 2007.
3. D. K. Pratihar , “Soft Computing” , Narosa, 2008.
4. Simon Haykin, “Neural Networks: A Comprehensive Foundation,” 2nd Edition, Prentice-Hall, 1999

Recommended Swayam NPTEL Courses:

1. Fuzzy Logic and Neural Networks by Prof. Dilip Kumar Pratihar , IIT Kharagpur
2. Fuzzy Sets, Logic and Systems & Applications, By Prof. Nishchal Kumar Verma, IIT Kanpur
3. Soft Computing Techniques, By Dr. T Subha , National Institute of Technical Teachers Training and Research (NITTTR), Taramani, Chennai.

Term Work: General Instructions

1. Batch wise tutorials are to be conducted. The number of students per batch should be as per University pattern for practical.
2. Students must be encouraged to write Python Programs in tutorial class only.
3. Each Student has to write at least 4 Python tutorials and at least 6 class tutorials on entire syllabus.
4. Suggested List for Python tutorials

   i. Write a program to evaluate various activation functions
   ii. Write a program for perceptron training algorithm and test it for two input AND & OR gate function
   iii. Write a program for training and testing of Multilayer Perceptron for two input EX-OR gate
   iv. Write a program for training and testing of Kohonen Self Organizing map for clustering application
   v. Write a program to do the Fuzzy Set Operations: AND, OR, D-Morgan’s theorem.
   vi. Write a program to find the fuzzy relations and compositions

The distribution of Term Work marks will be as follows

1	Attendance (Theory and Tutorial)	**05**
2	Class Tutorials on entire syllabus	**15**
3	Python Tutorials	**05**

Assessment:

Internal Assessment (IA) Test:

Assessment consists of two class tests of 20 marks each. The first class test (Internal Assessment I) is to be conducted when approx. 40% syllabus is completed and second class test (Internal Assessment II) when additional 35% syllabus is completed. Duration of each test shall be one hour.

End Semester Examination:

1. Question paper will comprise of total 06 questions, each carrying 15 marks.

2. Total 04 questions need to be solved.

3. Question No: 01 will be compulsory and should cover maximum content of the entire syllabus.

4. Remaining questions will be randomly selected from all the modules as per the weightage of each module (which is proportional to number of respective lecture hours mentioned in the syllabus).

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2304112	Microcontrollers	3	-	-	3	-	-	3

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Total					
2304112	Microcontrollers	20	20	40	60	2	--	--	100

Prerequisite:
Digital System Design

Course Objectives:

1. To acquire a comprehensive understanding of the essential components and systems that constitute microcomputers
2. To understand the underlying principles, structures, and functionalities of memory systems
3. To understand the architecture and instructions of 8051 microcontrollers.
4. To apply the knowledge of 8051 I/O ports, Timer, Counter, Interrupts, serial port and write the programs related to same
5. To understand the concepts of advanced microcontroller ARM7
6. To apply the concepts of embedded systems and recent microcontrollers

Course Outcomes:

After successful completion of the course student will be able to

1. Understand of the fundamental components and systems integral to microcomputers
2. Understand the memory systems to optimize system performance.
3. Understand the architecture and instructions of 8051 microcontrollers
4. Apply the concepts of I/O ports, Timer, Counter, Interrupts, serial port for 8051 microcontroller programming.
5. Understand the ARM7 microcontroller architecture and its suitability for embedded systems and real-time applications.
6. Apply the concept of embedded systems and recent advancements in microcontroller technologies and explore their impact on modern applications.

DETAILED SYLLABUS:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
0	**Prerequisite**	Digital system design	-	-
I	**Overview of Microprocessor based System**	**Overview of Components and Systems of Microcomputers** Introduction to the basic components of microcomputers, Significance of signal lines of memory. The actions performed by the microprocessor to fetch and execute instructions from memory after RESET. Concept of RISC & CISC Architecture. Harvard & Von Neumann Architecture. **Self-learning Topics:** Comparative study of different microprocessors	05	CO1
II	**The Memory Systems**	Concepts of primary memory and secondary memory, Need of Secondary memory. Types of Semiconductor Memory, Features of SRAM and DRAM. Cache Memory and its need. Concept of virtual memory, Segmentation and Paging.	04	CO2
III	**8051 Microcontroller & Assembly Language Programming**	Comparison between Microprocessor and Microcontroller. Features, architecture and pin configuration. CPU timing and machine cycle. Memory organization. Addressing modes. Instruction set. Need of Assembler & Compiler, Assembler Directives, Programs related to: arithmetic, logical operations.	09	CO3
IV	**8051 I/O Ports, Timer/Counters, Interrupts, Serial Port communication & Interfacing**	8051 Input / Output ports and Port structure. 8051 Timer/Counter, Timer modes. 8051 Interrupt, 8051 serial communication & modes. Assembly language program related to: delay subroutine, input & output port, timer, counter, serial port and Interrupt. Interfacing LEDs, Relay and switches.	09	CO4
V	**ARM7**	Introduction and Features of ARM 7, Architectural inheritance, Pipelining, Programmer's Model, Brief introduction of exception and interrupt handling. Concept of cortex A, cortex R and Cortex M. Instruction Set, Data processing, Data Transfer, Control flow.	08	CO5
VI	**Study 8 bit microcontroller Applications**	Definition of Embedded System, Embedded Systems Vs General Computing Systems. Factors to be Considered in Selecting a Microcontroller for an	04	CO6

		Application. Understanding features of ATMEGA 328, LPC 2148, MSP 430 and STM 32 Microcontrollers. Case Study on microcontroller based applications		

Text Books:

1. M. A. Mazidi, J. G. Mazidi and R. D. Mckinlay, “The 8051 Microcontroller & Embedded systems”, Pearson Publications, Second Edition 2006.
2. C. Kenneth J. Ayala and D. V. Gadre, “The 8051 Microcontroller & Embedded system using assembly & ‘C’ ”, Cengage Learning, Edition 2010
3. Raj Kamal, “Microcontrollers” Architecture, Programming, Interfacing and System Design”, Pearson Education India, Second Edition 2011
4. N. Senthil Kumar, M. S. P. S. A. Kumar, “Microprocessor and Microcontroller”, Oxford University Press, 2nd edition
5. I. Scott MacKenzie and Raphael C.W. Phan, “The 8051 Microcontroller”, Pearson Education International, 4th edition
6. Steve Furber, “ARM System on chip Architecture”, Pearson, 2nd edition

References:

1. “MCS@51 Microcontroller, Family User’s Manual” Intel
2. ATmega328P 8-bit AVR Microcontroller with 32K Bytes In-System Programmable Flash datasheet, Atmel
3. James A. Langbridge, “Professional Embedded Arm Development”, Wrox, John Wiley Brand& Sons Inc., Edition 2014
4. Andrew N. Sloss, Dominic Symes, Chris Wright, “ARM System Developer's Guide: Designing and Optimizing System Software", Elsevier
5. The 8051 Microcontroller and Embedded Systems, “Manish K Patel”, McGraw Hill 2014

Online References:

Sr. No.	Website Name
1.	Microprocessors and Microcontrollers By Prof. Santanu Chattopadhyay (IIT Kharagpur) - https://swayam.gov.in/nd1_noc20_ee42/preview
2.	PLC and Microcontroller By Dr Ritula Thakur (NITTTR, Chandigarh) - https://onlinecourses.swayam2.ac.in/ntr25_ed25/preview

Assessment:

Internal Assessment (IA) Test:

Assessment consists of two class tests of 20 marks each. The first class test (Internal Assessment I) is to be conducted when approx. 40% syllabus is completed and second class test (Internal Assessment II) when additional 35% syllabus is completed. Duration of each test shall be one hour.

End Semester Examination:

1. Question paper will comprise of total 06 questions, each carrying 15 marks.
2. Total 04 questions need to be solved.
3. Question No: 01 will be compulsory and should cover maximum content of the entire syllabus.
4. Remaining questions will be randomly selected from all the modules as per the weightage of each module (which is proportional to number of respective lecture hours mentioned in the syllabus).

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2304113	Analog and Digital Communication	3	-	-	3	-	-	3

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Total					
2304113	Analog and Digital Communication	20	20	40	60	2	--	--	100

Prerequisite:

- Elements of Telecommunication
- Electronic Devices and Linear Circuits
- Digital System Design

Course Objectives:

1. To demonstrate the fundamental concepts of Analog and Digital Communication Systems.
2. To understand various Analog and Digital Modulation and Demodulation techniques.
3. To analyze and compare the different Modulation and Demodulation techniques.
4. To explain the key concepts of Analog and Digital Pulse Modulation and Demodulation techniques.
5. To illustrate various source and channel coding techniques.
6. To examine the impact of noise and distortions in communication systems.

Course Outcomes:

After successful completion of the course student will be able to

1. Define the fundamental concepts of Analog and Digital Communication.
2. Explain the various Analog, Digital Modulation and Demodulation Techniques and Multiplexing Techniques.
3. Apply the concepts of information theory of source coding and channel coding techniques.
4. Analyse the various modulation and demodulation techniques.
5. Evaluate the performance of various error control codes.
6. Design an encoder and decoder for error control system with the given specifications.

DETAILED SYLLABUS:

Sr. No.	Name of Module	Detailed Content	Hours	CO Mapping
0	**Prerequisite**	Need for Modulation, Basics concepts of Amplitude and Frequency Modulation and Demodulation. Sampling Theorem and Pulse Code Modulation. Definition and waveforms of ASK, FSK and PSK.	**01**	**--**
I	**Introduction**	Block diagram of Analog and Digital Communication System, Signal-to-noise ratio, Noise factor, Noise Figure, Noise Temperature. Friis Formula. **Self-Learning Topics:** Types of Noise and Electromagnetic Spectrum.	**02**	**CO1**
II	**Analog Modulation and Demodulation**	**Amplitude Modulation and Demodulation-** Mathematical and Graphical Representation AM wave, Voltage distribution and Power Calculations. Basic Concepts of DSBSC and SSB. AM Diode Detectors - (Simple and Practical). **Self-Learning Topics:** Low level and High-level Transmitter.	**10**	**CO1** **CO2** **CO4**
		Frequency Modulation and Demodulation - Mathematical Representation of FM wave, deviation ratio, bandwidth requirement, Narrowband and Wideband FM, Pre-emphasis and De-emphasis, Noise-triangle, FM Generation (Varactor Diode and Armstrong method) , FM Detectors- Foster-Seeley Detector		
		Radio Receivers – Characteristics of radio receivers, Superheterodyne AM and FM receivers. **Self-learning Topics:** Use of AM and FM in Modern Communication Technology. Challenges faced by radio broadcasting industry.		
III	**Pulse Modulation and Multiplexing**	Sampling Theorem, Nyquist Criteria, Sampling Techniques, Aliasing error and Aperture effect.	**07**	**CO2** **CO4**
		Generation and Detection of PAM, PWM and PPM. PCM Transmitter and Receiver. Concepts of Delta modulation (DM) and Adaptive Delta Modulation (ADM).		
		Need of Multiplexing, Block Diagram explanation of TDM and FDM Systems. **Self-learning Topics:** Applications of Multiplexing		
IV	**Source Coding and Channel Coding**	Basics of Information Theory, Entropy, Shannon Hartley theorem Source Coding Techniques: Huffman coding and	**07**	**CO3** **CO5** **CO6**

	Techniques	Shannon-Fano coding. Channel Coding for Error Detection and Correction - Linear Block Codes and Convolutional Codes. **Self-learning Topics:** Applications of Source Coding and Channel Coding Techniques.		
V	**Baseband Transmission and Reception**	Block diagram of baseband Transmitter-Receiver system, Need of line codes, Properties, Types of line codes - RZ and NRZ Unipolar formats, RZ and NRZ Polar formats, RZ and NRZ Bipolar format (AMI format), Split phase Manchester format and Polar Quaternary formats. Inter Symbol Interference, Inter Channel Interference, Matched Filter. **Self-Learning Topics:** Equalizers	**04**	**CO1** **CO2**
VI	**Bandpass Transmission and Reception**	Generation, Detection, Error probability and Bandwidth of the following modulations: BASK, BFSK, BPSK, QPSK, offset QPSK, M-ary PSK, 16-ary QASK and MSK. **Self-learning Topics**: Applications of all the Modulation Techniques	**08**	**CO2** **CO4**

Text Books:

1. Kennedy and Davis “Electronics Communication System”, Tata McGraw Hill
2. Wayne Tomasi, “Electronics Communication Systems” Pearson Education, 5th Edition.
3. Herbert Taub, Donald L Schilling, Goutam Saha, “Principles of Communication Systems”, Tata McGraw Hill, 3rd Edition.
4. T. L. Singal, “Analog and Digital Communication,” Tata Mc-Graw Hill, New Delhi, First Ed.
5. Sklar B, and Ray P. K., “Digital Communication: Fundamentals and Applications,” Pearson, Dorling Kindersley (India), Delhi, Second Edition.

References:

1. Simon Haykin, “Communication System”, John Wiley and Sons,4th Ed.
2. B P Lathi, Zhi Ding, Modern Digital and Analog Communication Systems, Oxford University. 4th Ed.
3. Beharouz A. Forouzan, “Data Communication and Networking”, fourth edition.

Online References:

Sr. No.	Website Name
1.	Analog communication- https://swayam.gov.in/nd1_noc20_ee69/preview
2.	Principles of Digital Communication- https://nptel.ac.in/courses/108/101/108101113/
3.	Principles of Digital Communication- https://nptel.ac.in/courses/108/102/108102120/

Assessment:

Internal Assessment (IA) Test:

Assessment consists of two class tests of 20 marks each. The first class test (Internal Assessment I) is to be conducted when approx. 40% syllabus is completed and second class test (Internal Assessment II) when additional 35% syllabus is completed. Duration of each test shall be one hour.

End Semester Examination:

1. Question paper will comprise of total 06 questions, each carrying 15 marks.

2. Total 04 questions need to be solved.

3. Question No: 01 will be compulsory and should cover maximum content of the entire syllabus.

4. Remaining questions will be randomly selected from all the modules as per the weightage of each module (which is proportional to number of respective lecture hours mentioned in the syllabus).

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2304114	Microcontroller Laboratory	--	2	--	--	1	--	1

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical/ Oral	Total
		Internal assessment			End Sem. Exam			
		Test1	Test 2	Avg. of 2 Tests				
2304114	Microcontroller Laboratory	--	--	--	--	25	25	50

Prerequisite:
Digital System Design

Laboratory Objectives:

1. To evaluate and integrate development tools for effective system design and implementation for microcontroller based system
2. To study the addressing modes of 8051 and write the program to apply the same
3. To interface I/O devices to 8051 and write the program apply the same
4. To develop microcontroller based applications.

Laboratory Outcomes:

After successful completion of the course student will be able to:

1. Students will be able to utilize development tools to effectively design, simulate, and troubleshoot microcontroller-based systems.
2. Write assembly language programs for arithmetic and logical operations,
3. Write assembly language programs for code conversion & data transfer operations.
4. Write assembly language programs for general purpose I/O, Timers & Interrupts.
5. Interface & write programs for Input and Output devices.
6. Develop microcontroller based Applications.

List of Experiments:

Sr No	List of Experiments	Hrs
01	Explore the development tools to be used (Assembler, Linker, Compiler, Simulator, Emulator. IDE: like Keil, Edsim 51,tinkercad and wokwi etc	02
02	WAP in assembly language for 8051 to perform arithmetic operations such as: a) Addition b) Subtraction c) Multiplication d) Division	02
03	WAP in assembly language for 8051 to perform multiple byte decimal addition.	02
04	WAP in assembly language for 8051 to Exchange & moving block of elements. a) Exchange block from internal RAM to internal RAM. b) Moving block from internal RAM to external RAM.	02
05	WAP in assembly language for 8051 to Conversion of codes (Any One): a) Binary to BCD b) Binary to Gray etc	02
06	To write a program to arrange numbers in Ascending order or descending order	02
07	WAP in assembly language for 8051 To generate a) Square wave or b) Triangular wave	02
08	WAP in assembly language for 8051 for blinking LED. a) Using Timer and Interrupt b) Using Delay Subroutine.	02
09	Interfacing and Programming of 7 Segment display to 8051.	02
10	Interfacing and Programming of 16 x 8 LCD Display	02
11	Study of Stepper Motor. a) Continuous Mode. b) Step mode with 180 degree rotation. (Clockwise and Anticlockwise)	02
12	To write an ALP for 8051 Serial Communication to send string of characters on serial port.	02
13	Basic programming using ARM WAP to perform arithmetic operations such as: a) Addition b) Subtraction c) Multiplication d) Division	02
14	Small mini-Project Microcontroller based Applications on simulators like - Traffic Light Controller,Touchless door bell	02

Assessment:

Term Work: Term Work shall consist of at least 10 to 12 practical's based on the above list. Also, Term work Journal must include at least 2 assignments.

Term Work Marks: 25 Marks (Total marks) = 15 Marks (Experiment) + 5 Marks (Assignments) + 5 Marks (Attendance)

Practical & Oral Exam: An Oral & Practical exam will be held based on the above syllabus.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2304115	Analog and Digital Communication Laboratory	--	2	--	--	1	--	1

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical/ Oral	Total
		Internal assessment			End Sem. Exam			
		Test1	Test 2	Avg. of 2 Tests				
2304115	Analog and Digital Communication Laboratory	--	--	--	--	25	25	50

Prerequisite:

- Elements of Telecommunication
- Electronic Devices and Linear Circuits
- Digital System Design

Laboratory Objectives:

1. To demonstrate generation and detection of Analog and Digital Modulation techniques.
2. To demonstrate generation and detection of Pulse Modulation Techniques.
3. To learn source coding and error control coding techniques.
4. To compare different line coding methods.
5. To illustrate multiplexing techniques.
6. To use simulation tools for analog and digital communication techniques.

Laboratory Outcomes:

After successful completion of the course student will be able to:

1. Demonstrate various Analog Modulation and Demodulation techniques.
2. Demonstrate Pulse Modulation, Demodulation and Multiplexing techniques.
3. Demonstrate various Digital Modulation and Demodulation Techniques.
4. Evaluate the performance parameters of a communication system.
5. Examine various Line Coding Techniques.
6. Design different source coding and channel coding Techniques**.**

DETAILED SYLLABUS:

Sr. No.	Module	Detailed Content	Hours	LO Mapping
0	**Prerequisite**	--	--	--
I	**Introduction**	Noise voltage and Noise Power Calculations	02	LO1
II	**Analog Modulation and Demodulation**	DSBFC Modulation and Demodulation, DSBSC, Diode Detector FM Modulation and Demodulation, Pre-emphasis and De-Emphasis	06	LO1 LO4
III	**Pulse Modulation and Multiplexing**	Sampling Theorem Analog Pulse Modulation Techniques -PAM, PWM, PPM. Digital Pulse Modulation Techniques- PCM, DM, ADM Multiplexing Techniques -TDM and FDM	06	LO2
IV	**Source Coding and Channel Coding**	Source Coding Techniques- Shannon Fano Coding and Huffmann Coding Channel Coding Techniques - Linear Block Codes and Convolution Codes	04	LO6
V	**Baseband Transmission and Reception**	Line Codes, Matched Filter	02	LO4 LO5
VI	**Bandpass Transmission and Reception**	Modulation and Demodulation of ASK, FSK, PSK, QPSK Techniques	04	LO3

Text Books:

1. Kennedy and Davis “Electronics Communication System”, Tata McGraw Hill
2. Wayne Tomasi, “Electronics Communication Systems” Pearson Education, 5th Edition.
3. Herbert Taub, Donald L Schilling, Goutam Saha, “Principles of Communication Systems”, Tata McGraw Hill, 3rd Edition.
4. T. L. Singal, “Analog and Digital Communication,” Tata Mc-Graw Hill, New Delhi, First Ed.
5. Beharouz A. Forouzan, “Data Communication and Networking” fourth edition .

References:

1. Sklar B, and Ray P. K., “Digital Communication: Fundamentals and Applications,” Pearson, Dorling Kindersley (India), Delhi, Second Edition.
2.Simon Haykin, “Communication System”, John Wiley And Sons ,4th Ed.
3. B P Lathi, Zhi Ding, Modern Digital and Analog Communication Systems, Oxford University. 4th Ed.

Online Resources:

Sr. No.	Website Name
1.	Analog communication- https://swayam.gov.in/nd1_noc20_ee69/preview
2.	Principles of Digital Communication- https://nptel.ac.in/courses/108/101/108101113/
3.	Free/Libre and Open Source Software for Education project using cloud. scilab.in

List of Experiments:

Sr No	List of Experiments	Hrs
01	Generation and Detection of Amplitude Modulation	02
02	Generation and Detection of Frequency Modulation	02
03	Generation and Detection of DSBSC Signal.	02
04	Design and implementation of Pre-emphasis and De-emphasis circuit.	02
05	Verification of sampling theorem.	02
06	Generation of PAM Modulation /Demodulation.	02
07	Generation of PWM/PPM Modulation /Demodulation.	02
08	Demonstrate Digital Pulse Code Modulation Technique (PCM)	02
09	Demonstrate Delta Modulation and Adaptive Delta Modulation Techniques (DM, ADM)	02
10	Observation of Time Division multiplexing and De-multiplexing signals.	02
11	Observation of Frequency Division multiplexing and De-multiplexing signals.	02
12	Simulate Shannon-Fano Code and calculate code efficiency	02
13	Simulate Huffman code and calculate code efficiency.	02
14	Simulate Linear block code and find error detection capability.	02
15	Simulate Convolutional code as per given specification.	02
16	Observe and compare of various Line Codes.	02
17	Matched filter impulse response for a given input.	02
18	Modulation/Demodulation of Binary ASK.	02
19	Modulation/Demodulation of Binary FSK.	02
20	Modulation/Demodulation of Binary PSK.	02
21	Modulation/Demodulation of QPSK.	02
22	Generation (and detection) of MSK	02

Assessment:

Term Work: Term Work shall consist of at least 10 to 12 practicals' based on the above list. Also, Term work Journal must include at least 2 assignments.

Term Work Marks: 25 Marks (Total marks) = 15 Marks (Experiment) + 5 Marks (Assignments) + 5 Marks (Attendance)

Practical& Oral Exam: An Oral & Practical exam will be held based on the above syllabus.

Vertical – 4

Vocational and Skill Enhancement Course (VSEC)

Detail Syllabus

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2303611	C++ and Java Programming	-	4	-	-	2	-	2

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical/ Oral	Total
		Internal assessment			End Sem. Exam			
		Test1	Test 2	Avg. of 2 Tests				
2303611	C++ and Java Programming	--	--	--	--	50	25	75

Prerequisite:
C-Programming

Laboratory Objectives:

1. To introduce Object-Oriented Programming (OOP) principles and understand the necessity of OOP in software development using C++ and Java.
2. To develop problem-solving skills using control structures, functions, arrays, strings, and object-oriented programming concepts in C++.
3. To implement concepts like inheritance, polymorphism, operator overloading, file handling, and memory management in C++ and Java for better software design.
4. To explore Java programming paradigms and understand its differences from C++, focusing on Java classes, methods, inheritance, and polymorphism.
5. To familiarize students with advanced Java concepts like exception handling, multithreading, GUI programming, and applet development.

Laboratory Outcomes:

After successful completion of the course student will be able to

1. Demonstrate basic programming constructs such as data types, control statements, arrays, and strings in C++ and Java.
2. Apply object-oriented programming concepts such as classes, objects, encapsulation, inheritance, and polymorphism in software design.
3. Implement operator overloading, file handling, constructors, and destructors in C++ for efficient memory and resource management.
4. Develop Java applications using classes, objects, interfaces, exception handling, multithreading, and GUI programming.
5. Design and implement applet-based applications and GUI-based Java programs using AWT and event handling techniques.

DETAILED SYLLABUS

Sr. No.	Module	Detailed Content	Hours	LO Mapping
I		**Overview of CPP**	4	LO1
	1.1.	Need of Object-Oriented Programming (OOP), Object Oriented Programming Paradigm, Basic Concepts of Object-Oriented Programming, Benefits of OOP and C++ as object-oriented programming language.		
	1.2	C++ programming Basics, Data Types, Structures, Enumerations, control structures, Class, Object, class and data abstraction, class scope and accessing class members, separating interface from implementation, controlling access to members.		
		Prerequisites: **Students should have knowledge of** 1. **Basic Computer Knowledge** o Understanding how software works, how to run programs, compile code. 2. **Introduction to Programming** o Basic syntax and structure of C programming language o Familiarity with writing simple programs (input/output, variables, loops). 3. **Fundamentals of C Programming (Optional but helpful)** o Data types, variables, control structures (if, for, while). o Functions and arrays. **Self-Learning Topics** **Branching -** If statement, If-else Statement, Decision. **Looping –** while, do-while, for loop **Nested control structure-** Switch statement, Continue statement, Break statement.		
II		**C++ Function, Array and Strings**	6	LO1
	2.1	Returning values from functions. Reference arguments. Overloaded function. Inline		

		function. Default arguments. Return by reference		
	2.2	**Array and Strings** Concepts, Declaration, Definition, Accessing array element, One-dimensional and Multidimensional array. String, String Functions, standard C++ String class		
III		**Object-Oriented Programming using C++ and Files**	8	LO2, LO3
	3.1	**Operator Overloading-** concept of overloading, operator overloading, Overloading Unary Operators, Overloading Binary Operators, Data Conversion, Type casting (implicit and explicit), Pitfalls of Operator Overloading and Conversion, Keywords explicit and mutable. **Function-** Function prototype, accessing function and utility function, Constructors and destructors, Copy Constructor, Objects and Memory requirements, Static Class members, data abstraction and information hiding, inline function. **Constructor-** Definition, Types of Constructors, Constructor Overloading, Destructor.		
	3.2	**Inheritance-** Introduction, Types of Inheritance, Inheritance, Public and Private Inheritance, Multiple Inheritance, Ambiguity in Multiple Inheritance, Visibility Modes Public, Private, Protected and Friend, Aggregation, Classes Within Classes. Deriving a class from Base Class, Constructor and destructor in Derived Class, Overriding Member Functions, Class Hierarchies, **Polymorphism-** concept, relationship among objects in inheritance hierarchy, Runtime & Compile Time Polymorphism, abstract classes, Virtual Base Class.		
	3.3.	**File** -Stream in CPP, Class for File Stream Operation, Modes of Files, Opening and Closing File, Read, Write and append in File**.**		
IV		**Introduction to Java**	2	LO4
	4.1	Programming paradigms- Introduction to programming paradigms, Introduction to four main Programming paradigms like procedural, object oriented, functional, and logic & rule		

		based. Difference between C++ and Java		
	4.2	Java History, Java Features, Java Virtual Machine, Data Types and Size (Signed vs. Unsigned, User Defined vs. Primitive Data Types, Explicit Pointer type), Programming Language JDK Environment and Tools.		
V		**Inheritance, Polymorphism, Encapsulation using Java**	8	LO4
	5.1	**Classes and Methods**: class fundamentals, declaring objects, assigning object reference variables, adding methods to a class, returning a value, constructors, this keyword, garbage collection, finalize () method, overloading methods, argument passing, object as parameter, returning objects, access control, static, final, nested and inner classes, command line arguments, variable-length Arguments.		
	5.2.	Array, String, String buffer and Vectors		
	5.3	**Inheritances:** Member access and inheritance, super class references, Using super, multilevel hierarchy, constructor call sequence, method overriding, dynamic method dispatch, abstract classes, Object class. **Packages and Interfaces:** defining a package, finding packages and CLASSPATH, access protection, importing packages, interfaces (defining, implementation, nesting, applying), variables in interfaces, extending interfaces, instance of operator.		
VI		**Exception Handling and Applets in Java**	8	LO4, LO5
	6.1	**Exception Handling:** fundamental, exception types, uncaught exceptions, try, catch, throw, throws, finally, multiple catch clauses, nested try statements, built-in exceptions, custom exceptions (creating your own exception sub classes). **Multithreading Threading:** Introduction, thread life cycle, Thread States: new, runnable, Running, Blocked and terminated, Thread naming, thread join method, Daemon thread		
	6.2.	**Applet:** Applet Fundamental, Applet Architecture, Applet Life Cycle, Applet Skeleton, Requesting Repainting, status window, HTML Applet tag, passing parameters to Applets, Applet and Application Program		

	6.3	**GUI :** Introduction to AWT programming Layout and Component Managers Event handling.		
VII		**Mini Project**	3	LO2, LO3, LO4, LO5

Textbooks:

1. **E. Balagurusamy**, *"Object Oriented Programming with C++"*, Tata McGraw-Hill Education, **Sixth Edition**, 2013.
2. **D. Ravichandran**, *"Programming with C++"*, Tata McGraw-Hill Publishing Company Limited, **Second Edition**, 2006.
3. **Yashavant Kanetkar**, *"Let Us C++"*, BPB Publications, **Revised Edition**, 2020.
4. **E. Balagurusamy**, *"Programming with Java: A Primer"*, Tata McGraw-Hill Education, **Fifth Edition**, 2014.
5. **Cay S. Horstmann**, *"Core Java Volume I – Fundamentals"*, Pearson Education, **Eleventh Edition**, 2018.
6. **Kathy Sierra, Bert Bates**, *"Head First Java"*, O'Reilly Media, **Second Edition**, 2005.

References:

1. **Herbert Schildt**, *"Java: The Complete Reference"*, Tata McGraw-Hill Publishing Company Limited, **Ninth Edition**, 2014.
2. **Bjarne Stroustrup**, *"The C++ Programming Language"*, Addison-Wesley, **Fourth Edition**, 2013.
3. **Stanley B. Lippman, Josée Lajoie, Barbara E. Moo**, *"C++ Primer"*, Addison-Wesley, **Fifth Edition**, 2012.
4. **Scott Meyers**, *"Effective C++: 55 Specific Ways to Improve Your Programs and Designs"*, Addison-Wesley, **Third Edition**, 2005.
5. **Joshua Bloch**, *"Effective Java"*, Addison-Wesley, **Third Edition**, 2018.
6. **Sachin Malhotra, Saurabh Chaudhary** "Programming in Java", Oxford University Press, 2010.
7. **Grady Booch, James Rumbaugh, Ivar Jacobson,** "The Unified Modeling Languageser Guide", Pearson Education.

Software Tools for C++ and Java Programming

SrNo	Software Tools for C++ and Java Programming
1	**Visual Studio Code (VS Code)** ○ **Platform:** Windows, macOS, Linux ○ **Languages:** C++, Java (via extensions) ○ **Features:** Lightweight, IntelliSense, debugging, Git integration, customizable with extensions.
2	**Eclipse IDE** ○ **Platform**: Windows, macOS, Linux ○ **Languages:** Primarily Java, with C/C++ support (via CDT plugin)

	○ **Features:** Project management, code analysis, UI builder for Java apps.
3	**NetBeans IDE** ○ **Platform:** Windows, macOS, Linux ○ **Languages:** Java, C, C++ ○ **Features:** Excellent Java support, GUI builder, simple setup for projects
4	**IntelliJ IDEA** ○ **Platform:** Windows, macOS, Linux ○ **Languages:** Java, Kotlin, Scala, C++ (limited) ○ **Features:** Powerful Java IDE with smart code completion and refactoring tools.
5	**Code::Blocks** ○ **Platform:** Windows, macOS, Linux ○ **Languages:** C, C++ ○ **Features:** Lightweight C++ IDE, great for beginners, plugin support.
6	**Dev C++** ○ **Platform:** Windows ○ **Languages:** C, C++ ○ **Features:** Simple IDE with built-in compiler, perfect for learning C++.
7	**BlueJ** ○ **Platform:** Windows, macOS, Linux ○ **Languages:** Java ○ Features: Educational IDE designed for teaching OOP concepts.
8	**JGrasp** ○ **Platform:** Windows, macOS, Linux ○ **Languages:** Java, C, C++ ○ **Features:** Lightweight IDE with visualization tools for Java structures
9	**Xcode** ○ **Platform:** macOS ○ **Languages:** C, C++, Objective-C, Swift, Java (limited) ○ **Features:** Excellent for Apple ecosystem development; supports C++ well
10	**CLion (by JetBrains)** • **Platform:** Windows, macOS, Linux • **Languages:** C, C++ • **Features:** Smart C++ development with debugging, refactoring, CMake support.

Skill-Enhancement Activities for C++ and Java Programming
1. Use of Tools for programming and project development
Objective

- The objective is to enhance students' programming skills using industry-standard tools like Eclipse, NetBeans, IntelliJ IDEA, Code:Blocks, Dev C++, and BlueJ. It aims to familiarize them with project development, debugging, and external library integration. Training on tools like Maven prepares students for real-world software practices through hands-on, project-based learning.

Purpose

- To enhance programming skills and industry readiness, students should be trained to code using a variety of development environments and tools. **Eclipse**, **NetBeans**, and **IntelliJ IDEA** are widely accepted IDEs in the software industry, especially for Java development. These platforms support integration of **external libraries and frameworks**, project structuring, and version control. Students should also be introduced to **Maven** as a powerful build tool for managing project dependencies and builds efficiently.
- For C++ programming, students should gain hands-on experience using **Code::Blocks**, **Dev C++**, and **BlueJ**, which provide user-friendly interfaces and help build strong foundational knowledge in object-oriented programming and file handling. These tools also help in understanding how code compiles, links, and executes, making them ideal for beginners and intermediate learners. Emphasis should be placed on solving real-world problems, debugging, and project-based learning using these tools.

2. Real-World Mini Problem Statements via Industry Simulation

- **Objective:**
  Bridge the gap between academics and industry by simulating real-world development tasks aligned with **company-level expectations**.
- **Implementation Strategy:**
  - Identify **10–15 mini problem statements** from domains such as inventory management, attendance tracking, student feedback systems.
  - Organize students into **groups of 3–4** and assign one problem per group.
  - Allocate **8–12 hours (across the last two lab sessions)** for brainstorming, coding, testing, and demo.
  - Encourage the use of Eclipse, Maven, Git, and external APIs or libraries relevant to the solution.
- **Examples of Problem Statements:**
  - Build a **Leave Management System** using Java classes and file I/O.
  - Develop a **Library Management GUI** with Swing and JDBC.
  - Implement a **Patient Record Tracker** with JSON serialization and external libraries.
  - Create a **Book Recommendation Console App** using OOP and collections.
- **Expected Outcome:**
  - Students experience the **complete software development cycle**—from understanding a requirement to deploying a working solution.
  - Promotes **teamwork, time management, and tool proficiency**, building job-ready skills for campus placements.

Online Resources:

Sr. No.	Website Name (CPP Programming)
1.	**cplusplus.com -** Comprehensive reference for C++ syntax, standard libraries, and STL. Great for quick lookups http://www.cplusplus.com
2.	**GeeksforGeeks(C++) -** Rich in tutorials, quizzes, practice problems, and interview questions. Ideal for beginners to advanced learners https://www.geeksforgeeks.org/c-plus-plus
3.	**Codecademy – Learn C++ -** Interactive platform with real-time coding in browser. Gamified progress and projects included. https://www.codecademy.com/learn/learn-c-plus-plus
4.	**Coursera – C++ For C Programmers -** University-style course, great for structured learners. Includes peer-reviewed assignments and quizzes https://www.coursera.org/learn/c-plus-plus-a
.5.	**Udemy – Beginning C++ Programming - From Beginner to Beyond -** Covers both fundamentals and advanced concepts, Great for beginners and intermediate learners. https://www.udemy.com/course/beginning-c-plus-plus-programming/
6.	**NPTEL – Programming in C++-** Offered by IIT Kharagpur,Free to access, Comprehensive and academic-focused, Includes assignments, weekly quizzes, and final certification exam. https://nptel.ac.in/courses/106/105/106105151/
	Website Name (Java Programming)
1.	**JavaTpoint -** Easy-to-understand explanations, tons of examples, and hands-on exercises. Covers basics to frameworks. https://www.javatpoint.com/java-tutorial
2.	**Oracle Java Documentation -** The official and most authoritative resource on Java. Best for understanding the language in depth. https://docs.oracle.com/javase/tutorial/
3.	**W3Schools Java Tutorial-** Beginner-friendly and offers a try-it-yourself feature to code online. https://www.w3schools.com/java/
4.	**Coursera – Java Programming and Software Engineering Fundamentals -** Offered by Duke University,Beginner-friendly, includes real-world applications like web scraping and data analysis. https://www.coursera.org/specializations/java-programming
5.	**Udemy – Java Programming Masterclass updated to Java –** Over 80 hours of content, covering Java from basics to advanced, Taught by experienced software engineer https://www.udemy.com/course/java-the-complete-java-developer-course/
6.	**NPTEL – Object-Oriented Programming using Java** https://nptel.ac.in/courses/106/105/106105191/ - Offered by IIT Kharagpur, In-depth academic course with real-life applications and Java-specific concepts,Includes weekly assignments, lectures,

List of Experiments.

Problems Statement can be divided in three parts

1. Some Few can be solved in class during Lecture so involvement of Students will increase
2. Some Few can be given as Assignment so that repeated process will retain the syntax and logic
3. Some can be asked to solve while doing the practical session

Note : Out of the given list topic wise 25% can be solved in class, 50% can be taken in Lab and remaining 25% can be given as assignment

Unit	Topic	LO
1.0	1. Develop a C++ program to demonstrate the concept of class and object by creating a simple "Bank Account" management system. 2. Create a program that uses structures and enumerations to store and display student information. 3. Implement a C++ application that showcases the use of encapsulation by creating a class for employee data management. 4. Write a program to demonstrate polymorphism using function overloading and operator overloading. 5. Design a C++ program to calculate the area of different shapes (Circle, Rectangle, Triangle) using function overloading. 6. Create a C++ program to simulate a simple library management system using classes and objects. 7. Implement a program using default arguments and inline functions to calculate the volume of different geometric shapes. 8. Develop a C++ application to demonstrate the use of reference arguments in a function for swapping two numbers. 9. Write a C++ program that uses the concept of separating interface from implementation by creating a class for basic arithmetic operations. 10. Build a simple program to demonstrate returning values by reference in C++ using a class to manage complex numbers. **Objective :** These problem statements and objectives cover various concepts from OOP, including encapsulation, polymorphism, data abstraction, and other C++ programming concepts.	LO1
2.0	**1.** Write a C++ program to find the largest of three numbers	LO1

	using an if-else statement**.** 2. Develop a program to check whether a given number is prime using a while loop**.** 3. Create a C++ program that simulates a simple menu-driven calculator using a switch statement**.** 4. Implement a program to print the Fibonacci series using a do-while loop. 5. Write a C++ program to display numbers from 1 to 100, but skip multiples of 5 using the continue statement. **6.** Develop a C++ program to input and display elements of a one-dimensional array**.** **7.** Write a C++ program to perform matrix addition using a two-dimensional array**.** 8. Create a program to check if a given string is a palindrome using standard C++ string functions. 9. Implement a C++ program to count the number of vowels and consonants in a given string. 10. Design a simple student management system using structures to store student details and display information. **Objective :** To develop problem-solving skills and logical thinking by applying C++ control statements, arrays, and string manipulation techniques to create efficient and optimized programs.	
3.0	1. Create a C++ program to demonstrate operator overloading for adding two complex numbers using the + operator. 2. Implement a program to overload the ++ operator for incrementing the values of a custom class object. 3. Develop a C++ application that demonstrates explicit type casting using constructors. 4. Write a C++ program that demonstrates the use of mutable keyword for modifying a constant object. 5. Design a class with a copy constructor to create a duplicate of an existing object.. 6. Create a program using single inheritance to derive a class Student from a base class Person. 7. Write a C++ program that demonstrates multiple inheritance by creating a class that inherits from two base classes. 8. Develop a program to illustrate the concept of virtual base class to solve the diamond problem. Implement a C++ program that demonstrates runtime polymorphism using virtual functions.. 11. Create a base class Shape with a virtual function draw()	• Practical 1–4 → LO2 • Practical 5 → LO3 • Practical 6–10 → LO2 • Practical 11–15 → LO3

	and derive classes like Circle and Rectangle to override the function. Write a C++ program to read and write data to a file using file streams.. 12. Implement a simple file-handling application to append data to an existing file. 13. Create a student management system where student details are stored and retrieved from a file.. 14. Develop a C++ program to count the number of words in a given text file. 15. Implement a C++ application to copy content from one file to another. **Objective :** To understand and apply object-oriented programming concepts such as operator overloading, inheritance, polymorphism, and file handling in C++ to develop efficient and maintainable applications.	
4.0	1. Develop a Java program to demonstrate the procedural programming approach using simple arithmetic operations. 2. Implement a class in Java to demonstrate object-oriented programming by creating a Student class with attributes and methods. 3. Create a Java program to illustrate functional programming using lambda expressions and streams. 4. Write a simple rule-based expert system in Java using conditional statements to suggest clothing based on weather input. 5. Compare and contrast C++ and Java by implementing a simple calculator program in both languages. 6. Develop a Java program to demonstrate different data types and their sizes using simple variables. 7. Write a Java program to convert an integer from signed to unsigned using bitwise operations. 8. Create a simple Java application using JDK and explain the development process including compiling and running using javac and java commands. 9. Develop a Java program to illustrate the concept of explicit pointers using references. 10. Implement a Java program that simulates the working of a Java Virtual Machine (JVM) by creating and running multiple threads. **Objective :** To understand the fundamentals of Java programming, including programming paradigms, Java history,	LO4

	features, data types, JVM functionality, and the use of JDK tools for developing efficient applications.	
5.0	1. Write a Java program to demonstrate class fundamentals by creating a class Employee with attributes and methods. 2. Develop a Java program to demonstrate the use of this keyword to differentiate between instance variables and parameters. 3. Create a Java program that uses static methods and variables to calculate the area of a rectangle. 4. Implement a Java program to demonstrate method overloading using different parameter types. 5. Write a program to demonstrate the use of garbage collection and the finalize() method. 6. Develop a Java program to create and manipulate arrays by finding the largest element. 7. Write a Java program to perform basic string operations using the String class. Create a Java program to demonstrate the use of StringBuffer to reverse a given string. 8. Implement a program to simulate a simple task management application using Vectors. 9. Write a Java program to concatenate two strings using StringBuilder.. 11. Develop a Java program to demonstrate single inheritance using a Person class and a Student class. 12. Create a program to illustrate the concept of method overriding using a parent and child class. 13. Write a Java program using multiple inheritance through interfaces to implement a simple vehicle management system. 14. Develop a Java program to create a package named MyPackage and import it in another program. 15. Implement a Java program using the instanceof operator to check object types in an inheritance hierarchy. **Objective :** To apply the principles of object-oriented programming in Java by implementing concepts of inheritance, polymorphism, encapsulation, class management, interfaces, and file handling to build robust and scalable applications.	LO4
6.0	1. Write a Java program to demonstrate exception handling using try, catch, and finally blocks. 2. Develop a Java program to create a custom exception class and handle it using throw and throws keywords..	• Practical 1–5 → LO5 (Exception Handling) • Practical 6–10 → LO5 (Multithreading) • Practical 11–15 → LO5

	3. Create a Java application that handles multiple exceptions using multiple catch blocks. 4. Write a Java program to demonstrate nested try statements. 5. Implement a program to simulate uncaught exceptions and analyze its impact.. 6. Write a Java program to demonstrate thread creation using Runnable and Thread classes. 7. Develop a Java program to implement thread synchronization using synchronized methods. 8. Create a Java application to demonstrate inter-thread communication using wait(), notify(), and notifyAll(). 9. Write a Java program to demonstrate thread priorities and daemon threads.. 10. Implement a program to show the lifecycle of a thread using different states. 11. Create a simple Java applet that displays a welcome message using the paint() method 12. Develop a Java applet to handle mouse events and display coordinates. 13. Write a Java program to create a graphical user interface using AWT components. 14. Implement a Java applet that takes parameters from HTML using the Applet tag. 15. Create a simple drawing application using AWT that allows the user to draw shapes. **Objective :** To understand and implement advanced Java concepts such as exception handling, multithreading, applet programming, and graphical user interface (GUI) development using AWT, enhancing application reliability and user interactivity.	(Applets and GUI)

List of Mini project

Sr No	List of Mini Projects (CPP)	LO
01	**Student Report Card Management System** • **Concepts Used:** File Handling, Classes, Structures, Constructors • **Description:** Add, delete, modify, and view student academic records. Data is stored in files.	LO2, LO3
02	**Library Management System** • **Concepts Used:** File Handling, OOP, Arrays, Structures	LO2, LO3

	• **Description:** Manage books in a library (add/remove/search). Track issued and returned books.	
03	**Bank Management System** • **Concepts Used:** File Handling, Classes & Objects, Data Abstraction • **Description:** Simulate banking operations like opening an account, deposits, withdrawals, and account balance inquiry.	LO2, LO3
04	**Inventory Management System** • **Concepts Used:** File I/O, Object-Oriented Design, Polymorphism • **Description:** Track product stock, purchase and sales, and maintain transaction logs.	LO2, LO3
05	**Hotel Reservation System** • **Concepts Used:** File Handling, Class Inheritance, Constructors • **Description:** Manage room bookings, cancellations, and availability checks with cost estimation.	LO2, LO3
06	**Employee Payroll Management System** • **Concepts Used:** File Handling, Inheritance, Virtual Functions • **Description:** Calculate salary, taxes, bonuses, and store payroll details of employees.	LO2, LO3
07	**Clinic Patient Record System** • **Concepts Used:** File Streams, Class Hierarchy, Object Persistence • **Description:** Maintain patient records, appointment scheduling, and doctor allocation.	LO2, LO3
08	**Online Quiz Management System** • **Concepts Used:** File Handling, Menus, Functions, Classes • **Description:** Conduct multiple quizzes, store scores, and allow user login with progress tracking.	LO2, LO3
09	**Simple Railway Ticket Booking System** • **Concepts Used:** File I/O, Structures, Functions • **Description:** Simulate booking, cancellation, and displaying train details using file storage.	LO2, LO3
10	**Book Store Management System** • **Concepts Used:** File Handling, OOP Principles, Sorting and Searching • **Description:** Add/update/search/delete book details, generate billing and inventory reports.	LO2, LO3
	List of Mini Projects (JAVA)	
01	**Expense Tracker** **Problem Statement:** Build a desktop application where users can log daily expenses, categorize them (food, travel, etc.), and view monthly summaries. Data should be stored and retrieved from local files GUI Interface.	LO4, LO5
02	**Recipe Book Manager** **Problem Statement:** Create a GUI app where users can store, search, and edit their favorite recipes. Each recipe should be saved as an individual file or organized into categories using folders.	LO4, LO5

03	**Bug Tracker Tool** **Problem Statement:** Build a mini bug-tracking system where users can log, update, and mark bugs as resolved. Save bug reports to a file and display them in a sortable GUI table.	LO4, LO5
04	**Fitness and Workout Logger** **Problem Statement:** Allow users to create workout plans, log completed exercises, and track progress through charts or stats (optional). File handling should maintain history and progress logs and GUI.	LO4, LO5
05	**Event Scheduler and Reminder** **Problem Statement:** Design a scheduling system to plan events and get pop-up reminders. Save and load event lists using file handling, optionally integrating a basic calendar UI.	LO4, LO5
06	**Simple Customer Feedback Collector** **Problem Statement:** Build a feedback form where users submit their opinions on products or services. Responses should be saved in structured format (CSV/JSON) for analysis later and GUI.	LO4, LO5
07	**Contact Book with Export Feature** **Problem Statement:** Implement a GUI-based contact book that allows adding/editing/deleting contacts and exporting data to a .csv file for external use.	LO4, LO5
08	**Parking Lot Management System** **Problem Statement:** Simulate a parking lot with slots for vehicles. Allow entry/exit registration, generate parking slips, and save logs of all vehicles using file handling and GUI.	LO4, LO5
09	**Daily Mood Tracker** **Problem Statement:** Let users record their mood daily with a short note. Store entries in files and allow users to browse past entries and see frequency stats of mood types using GUI.	LO4, LO5
10	**Digital Notes Organizer** **Problem Statement:** Create an app to manage and organize personal notes. Provide options to add, delete, and edit notes, with autosave features and organized storage using file structures and GUI.	LO4, LO5

Online Repository

SrNo	Respository

1	**GitHub**
	• **Link:** https://github.com
	• **Reason to use It:**
	  ○ Largest open-source platform with thousands of C++ and Java projects.
	  ○ Great for exploring real-world applications, contributing to open source, and version control.
	  ○ Students can fork repositories, collaborate on code, and showcase projects for placements.

2	**GeeksforGeeks – Practice and Code Repository**
	• **Link:** https://practice.geeksforgeeks.org/
	• **Reason to use It:**
	  ○ Rich in C++ and Java examples, coding problems, and data structures.
	  ○ Covers programming concepts with working source code.
	  ○ Regularly updated with interview questions and competitive coding challenges.

3	**GitLab**
	• **Link:** https://gitlab.com
	• **Reason to use It:**
	  ○ Similar to GitHub but with more private repositories (ideal for academic use).
	  ○ Good for hosting collaborative coding projects and using CI/CD pipelines.
	  ○ Supports C++ and Java with various development tools.

4	**SourceForge**
	• **Link:** https://sourceforge.net
	• **Reason to use It:**
	  ○ Repository of open-source software including tools, utilities, and programming frameworks.
	  ○ Many Java GUI-based and C++ utility projects are available for download and modification.
	  ○ Ideal for exploring legacy and niche programming projects.

5	**CodeChef GitHub Repository (and Platform)**
	• **Link:**
	  ○ CodeChef: https://www.codechef.com/
	  ○ GitHub Repo: https://github.com/codechef
	• **Reason to use It:**
	  ○ Offers a massive problem-solving community for C++ and Java.
	  ○ Students can practice competitive programming and refer to community-driven solutions.
	  ○ Excellent for mastering logic and problem-solving patterns.

Term Work:

- At least **12** experiments (**06 experiments** each on **C++** and **JAVA**) covering entire syllabus should be set to have well predefined inference and conclusion. Teacher should refer the suggested experiments and can design additional experiment to maintain better understanding and quality.
- The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative.
- Term work assessment must be based on the overall performance of the student with every Experiments are graded from time to time.
- The grades will be converted to marks as per "**Attendance+performance+submission+Viva/MCQ Test**" and should be added and averaged. Based on above scheme grading and term work assessment should be done.

- The practical and oral examination will be based on entire syllabus. Students are encouraged to share their experiments codes on online repository. Practical exam should cover all **12** experiments for examination.
- Mini project either in CPP or java from the topic given or any other topic of same level which should include construct of CPP/Java, File handling and GUI is mandatory.

Assessment:

Term Work: Term Work shall consist of at least 10 to 12 practicals' based on the above list and Mini project. Also, Term work Journal must include at least 4 (2 CPP+ 2Java) assignments.

Term Work Marks: 50 Marks (Total marks) = 10 (Attendance) + 10 (Performance in Lab) + 10 (Timely Submission) + 20 (Viva or MCQ Test). MCQ test can be conducted using online system for which10-15 min can be allocated in every practical.

Practical& Oral Exam: An Oral & Practical exam will be held based on the above syllabus.

AC –
Item No. –

As Per NEP 2020

University of Mumbai

Syllabus for HSSM Vertical 5		
Faculty of Engineering		
Board of Studies in Under Engineering		
Second Year Programme in HSSM– Common to All Branches		
Semester	III & IV	
Title of Paper (Lab)		Credits
I) Entrepreneurship Development	III	2
II) Environmental Science	III	2
III) Business Model Development	IV	2
IV) Design Thinking	IV	2
Total Credits		8
From the Academic Year		2025-26

Sem. - III

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2993511	Entrepreneurship Development	--	2*+2	-	-	2*+2	-	2

Course Code	Course Name	Examination Scheme						
		Theory Marks				Term Work	Practical/ Oral	Total
		Internal assessment			End Sem. Exam			
		IAT-I	IAT-II	IAT-I + IAT-II				
2993511	Entrepreneurship Development	--	--	--	--	50	--	50

Note: * Two hours of practical class to be conducted for full class as demo/discussion/theory.

Lab Objectives:

1. To introduce students to entrepreneurship concepts and startup development.
2. To develop business idea generation, validation, and business model preparation.
3. To provide hands-on experience in market research, financial planning, and business pitching.
4. To enhance problem-solving and decision-making skills in entrepreneurial ventures.
5. To familiarize students with government schemes and support systems for entrepreneurs.
6. To develop communication and presentation skills required for business pitching.

Lab Outcomes:

Upon successful completion of this course, students will be able to:

1. Understand the fundamental concepts of entrepreneurship and business models.
2. Conduct market research and develop business plans.
3. Utilize financial planning and cost analysis for startups.
4. Apply entrepreneurial skills to identify and solve business challenges.
5. Develop prototypes using open-source software for business operations.
6. Pitch business ideas effectively with structured presentations.

DETAILED SYLLABUS

Sr. No.	Module	Detailed Content	Hours	LO Mapping
0	**Prerequisite**	Fundamentals of communication and leadership skills.	01	--
I	**Introduction to Entrepreneurship**	Definition, Characteristics, and Types of Entrepreneurs. Entrepreneurial Motivation and Traits. Start-up Ecosystem in India. Challenges in Entrepreneurship	**02**	**LO1**
II	**Business Idea Generation &**	Ideation Techniques: Design Thinking, Brainstorming, Mind	**04**	**LO2**

	Validation	Mapping. Business Model Canvas (BMC). Market Research & Customer Validation. Minimum Viable Product (MVP) Concept.		
III	**Business Planning & Strategy**	Writing a Business Plan. SWOT Analysis and Competitive Analysis. Financial Planning and Budgeting. Risk Assessment and Management	**04**	**LO3**
IV	**Funding and Legal Framework**	Sources of Funding: Bootstrapping, Angel Investors, Venture Capital Government Schemes & Start-up India Initiatives. Business Registration & Legal Formalities. Intellectual Property Rights (IPR) & Patents	**05**	**LO4**
V	**Marketing & Digital Presence**	Branding and Digital Marketing. Social Media Marketing & SEO. Customer Relationship Management (CRM). E-commerce & Online Business Models	**05**	**LO5**
VI	**Business Pitching & Prototype Development**	Pitch Deck Preparation & Presentation Techniques. Prototyping with Open-source Tools. Elevator Pitch & Investor Pitch. Case Studies of Successful Start-ups	**05**	**LO6**

Text Books:

1. "Entrepreneurship Development and Small Business Enterprises" – Poornima M. Charantimath, Pearson, 3rd Edition, 2021.
2. "Innovation and Entrepreneurship" – Peter F. Drucker, Harper Business, Reprint Edition, 2019.
3. "Startup and Entrepreneurship: A Practical Guide" – Rajeev Roy, Oxford University Press, 2022.
4. "Essentials of Entrepreneurship and Small Business Management" – Norman Scarborough, Pearson, 9th Edition, 2021.
5. "The Lean Startup" – Eric Ries, Crown Publishing, 2018.

References:

1. "Disciplined Entrepreneurship: 24 Steps to a Successful Startup" – Bill Aulet, MIT Press, 2017.
2. "Zero to One: Notes on Startups, or How to Build the Future" – Peter Thiel, 2014.
3. "The $100 Startup" – Chris Guillebeau, Crown Business, 2019.
4. "Business Model Generation" – Alexander Osterwalder & Yves Pigneur, Wiley, 2020.
5. "Blue Ocean Strategy" – W. Chan Kim & Renée Mauborgne, Harvard Business Review Press, 2019.

Online Resources:

Website Name
1. Startup India Portal – https://www.startupindia.gov.in 2. MIT OpenCourseWare – Entrepreneurship – https://ocw.mit.edu/courses/sloan-school-of-management/ 3. Coursera – Entrepreneurship Specialization – https://www.coursera.org/specializations/entrepreneurship

4. Harvard Business Review – Entrepreneurship Articles – https://hbr.org/topic/entrepreneurship
5. Udemy – Startup & Business Courses – https://www.udemy.com/courses/business/entrepreneurship/

List of Experiments.

Sr No	List of Experiments	Hrs
01	Business Idea Generation using Mind Mapping.	02
02	Conducting Market Research & Customer Validation.	02
03	Preparing a Business Model Canvas for a Startup Idea.	02
04	Developing a Financial Plan & Break-even Analysis.	02
05	Creating a Website using WordPress/Wix.	02
06	Social Media Marketing Campaign using Open-source Tools.	02
07	Digital Prototyping using Figma/Inkscape.	02
08	Business Pitch Deck Preparation & Presentation.	02
09	Exploring Government Schemes for Startups.	02
10	Legal Compliance & IPR Basics (Case Study).	02

Sr No	List of Assignments / Tutorials	Hrs
01	a. Write a report on any successful entrepreneur and their startup journey. b. Conduct SWOT analysis for a real-life startup.	02
02	Develop a business idea and create a one-page business plan.	02
03	Conduct market research using surveys & present findings.	02
04	Design a simple logo and branding strategy for a startup.	02
05	Create a financial model and cost estimation for a startup.	02
06	Make a case study report on startup failure analysis.	02

List of Open-Source Software
1. Canva – Designing pitch decks, social media posts, and branding materials. 2. Trello / Asana – Project management for startups. 3. GIMP / Inkscape – Graphic design and logo creation. 4. WordPress / Wix – Website development for startups. 5. OpenCart / PrestaShop – E-commerce website setup. 6. Figma – UI/UX design and prototyping. 7. LibreOffice Calc – Financial planning and budgeting. 8. Google Suite (Docs, Sheets, Slides) – Documentation and presentations. 9. Python (Pandas, Flask, Django) – Data analytics and web application development. 10. MailChimp – Email marketing and customer engagement.

Assessment :
Term Work: Term Work shall consist of at least 08 to 10 practicals' based on the above list. Also, Term work Journal must include at least 6 assignments.
Term Work Marks: 50 Marks (Total marks) = 20 Marks (Experiment) + 15 Marks (Assignments) + 5 Marks (Attendance)+ 10 Marks (Report)

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract.	Tut.	Theory	Pract.	Tut.	Total
2993512	Environmental Science	--	2*+2	-	--	2*+2	-	2

		Theory					Term work	Pract / Oral	Total
		Internal Assessment			End Sem Exam	Exam Duration (in Hrs)			
		IAT-I	IAT-II	IAT-I+IAT-II					
2993512	Environmental Science	--	--	--	--	--	50	--	50

Note: * Two hours of practical class to be conducted for full class as demo/discussion/theory.

Rationale:

Most of the engineering branches are offspring of applied sciences, and their practices have a significant impact on the environment. Understanding environmental studies is essential for engineers to develop sustainable solutions, minimize ecological footprints, and promote responsible resource management. This course equips students with the knowledge of ecosystems, biodiversity, pollution control, and environmental laws, enabling them to integrate sustainability into engineering practices.

Lab Objectives:

1. To understand the scope, importance, and role of environmental studies in public awareness and health.
2. To study different natural resources, their issues, and sustainable conservation.
3. To understand ecosystem types, structures, and functions.
4. To explore biodiversity, its importance, threats, and conservation.
5. To learn about pollution types, causes, effects, and control measures.
6. To understand environmental challenges, sustainability, and ethics.

Lab Outcomes:

1. Explain the significance of environmental studies and the role of IT in environment and health.
2. Describe resource types, associated problems, and conservation methods.
3. Classify ecosystems and explain their role in ecological balance
4. Analyze biodiversity levels and conservation strategies, especially in India.
5. Explain pollution impacts and suggest preventive measures.
6. Discuss environmental issues and propose sustainable solutions.

DETAILED SYLLABUS:

Unit Name	Topic Name	Topic Description	Hours	LO Mapping

I	**The Multidisciplinary Nature of Environmental Studies**	Definition, scope and importance. Need for public awareness, Role of information technology in environment and human health. Human population and the environment: Population growth, variation among nations. Population Explosion- family welfare program. Environment and human health Women and child welfare	**03**	LO1
II	**Natural Resources**	Renewable and non-renewable resources. Natural resources & associated problems: a) Forest resources: b) Water resources: Natural resources & associated problems c) Mineral resources: d) Food resources: e) Energy resources: Role of an individual in conservation of natural resources: f) Equitable use of resources for sustainable lifestyles.	**04**	LO2
III	**Ecosystems**	Concepts of an ecosystem. Introduction, types, characteristic features, structure and function of the following ecosystem: a. Forest ecosystem b. Grassland ecosystem c. Desert ecosystem d. Aquatic ecosystem (ponds, streams, lakes, rivers, oceans, estuaries). Case study on various ecosystems in India.	**05**	LO3
IV	**Biodiversity and its Conservation**	Introduction-Definition: genetic species and ecosystem diversity. Bio-geographical classification of India Value of biodiversity : Consumptive use, productive use, social, ethical, aesthetic and option values, Bio-diversity at global, national, local levels India as a mega diversity nation Case study on Bio diversity in India.	**05**	LO4
V	**Environmental Pollution Definition**	Causes, effects and control measures of: a) Air pollution b) Water pollution c) Soil pollution. Solid waste management: Causes, effect and control measures of urban and industrial wastes. Role of an individual in prevention of pollution, Case study on Pollution Disaster management: floods, earthquake, cyclone and landslides. Carbon Credits for pollution prevention	**05**	LO5

VI	**Social Issues and Environment**	From unsustainable to sustainable development Urban problems related to energy, Water conservation, rain water harvesting, watershed management. Environmental ethics: issues and possible solution. Climate change, global warming, acid rain, ozone layer depletion, nuclear accidents and holocaust. Case studies. Consumerism and waste products. Environment protection act. Public awareness Case study on Environmental Ethics	**04**	LO6

Textbooks

1. Environmental Science: Towards a Sustainable Future, G. Tyler Miller and Scott Spoolman, 13th Edition, Cengage Learning 2021
2. Environmental Management: Text and Cases, Bala Krishnamoorthy, 3rd Edition, PHI Learning, Publication Year: 2016
3. Green IT: Concepts, Technologies, and Best Practices, Markus Allemann, Springer 2008
4. Sustainable IT: Slimming Down and Greening Up Your IT Infrastructure, David F. Linthicum, IBM Press 2009
5. Environmental Modelling: Finding Solutions to Environmental Problems, David L. Murray, Cambridge University Press 2016
6. Remote Sensing and Image Interpretation, Thomas M. Lillesand, Ralph W. Kiefer, and Jonathan W. Chipman, 9th Edition, John Wiley & Sons 2020
7. Business Ethics: Concepts and Cases, Manuel Velasquez, Pearson 2012

Reference Books

1. Environmental Law and Policy in India, Shyam Divan and Armin Rosencranz, 2nd Edition, Oxford University Press 2018
2. The International Handbook of Environmental Laws, David Freestone and Jonathon L. Rubin, Edward Elgar Publishing 2000
3. E-Waste Management: Challenges and Opportunities in Developing Countries, Ruediger Kuehr and Ram K. Jain, Springer 2008
4. The E-Waste Handbook: Managing Electronic Waste, Klaus Hieronymi, Ruediger Kuehr, and Ram K. Jain, Earthscan 2009
5. Environmental Ethics: An Introduction, J. Baird Callicott, University of Georgia Press1999

Online References:

Sr. No.	Website Name
1.	Centre for Science and Environment (CSE), Website: cseindia.org
2.	Ministry of Environment, Forest and Climate Change (MoEFCC), Government of India
3.	CSIR-National Environmental Engineering Research Institute (NEERI)

List of Experiments.

Sr No	List of Experiments	Hrs

01	Study of Environmental Components and Ecosystems.	2
02	Visit and Report on Solid Waste Management Plant.	2
03	Study of Renewable Energy Sources (Solar, Wind, Biogas).	2
04	Analysis of Air and Water Quality Parameters.	2
05	Study of Local Biodiversity and Conservation Methods.	2
06	Awareness Activity on Environmental Issues.	2
07	Rainwater Harvesting System Design	2
08	Case Study on Environmental Pollution & Control Measures.	2
09	Report on Climate Change Impact and Adaptation.	2
10	Study of Environmental Laws and Acts.	2
11	Study of Disaster Management Techniques.	2
12	Report on Role of IT in Environmental Protection.	2

Sr No	List of Assignments / Tutorials	Hrs
01	Prepare a report on Renewable and Non-Renewable Resources.	2
02	Write a case study on Ecosystem Types in India	2
03	Write a report on Biodiversity in India.	2
04	Prepare a report on Pollution Types and Control Measures.	2
05	Prepare a report on Environmental Ethics and Sustainability.	2
06	Prepare a case study report on Global Warming and Climate Change.	2
07	Report on Role of an Individual in Environmental Protection.	2
08	Write a report on Disaster Management Techniques.	2
09	Prepare a report on Environmental Laws and Acts in India.	2
10	Case Study on E-waste Management and Recycling Techniques.	2

Assessment :

Term Work: Term Work shall consist of at least 10 to 12 practical's based on the above list. Also, Term work Journal must include at least 8 to 10 assignments.

Term Work Marks: 50 Marks (Total marks) = 20 Marks (Experiment) + 15 Marks (Assignments) + 5 Marks (Attendance)+ 10 Marks (Report)

AC – 20/04/2024
Item No. – 6.6 Sem. I (6b)

As Per NEP 2020

University of Mumbai

Syllabus for Basket of Open Elective Courses	
Board of Studies in Computer Science	
UG First Year Programme	
Semester	**I**
Title of Paper	**Credits 2/ 4**
I) Web Designing	**2**
	2
From the Academic Year	**2024 – 2025**

Open Elective Courses

Name of the Course: Web Designing

Sr. No.	Heading	Particulars
1	**Description the course:**	**Introduction:** This course offers a comprehensive exploration of web development, covering essential technologies such as HTML, CSS, JavaScript, and PHP. Students will gain practical skills and knowledge necessary to create dynamic and visually appealing websites. **Relevance:** In today's digital age, web development skills are in high demand across various industries. Understanding HTML, CSS, JavaScript, and PHP is crucial for anyone interested in pursuing a career in web development or related fields. **Usefulness:** The skills acquired in this course are highly transferable and applicable in a wide range of professional settings. Whether students aim to become web developers, designers, or entrepreneurs, proficiency in web development technologies is invaluable. **Application:** Students will learn to apply their knowledge of HTML, CSS, JavaScript, and PHP to develop interactive websites and web applications. Through hands-on projects and exercises, they will gain practical experience in building real-world solutions. **Interest:** The course content is designed to engage students with a passion for technology and creativity. From creating visually appealing designs to implementing dynamic functionality, students will find ample opportunities to explore and express their interests. **Connection with Other Courses:** This course serves as a foundation for further studies in web development and related disciplines. It complements other technology courses by providing essential skills and knowledge that can be applied across

		various domains. **Demand in the Industry:** The demand for skilled web developers continues to grow as businesses and organizations increasingly rely on their online presence. Graduates of this course will be well-equipped to meet this demand and contribute effectively in the industry. **Job Prospects:** Completion of this course opens up numerous job opportunities in web development, design, digital marketing, e-commerce, and more. With the skills acquired, students can pursue roles such as front-end developer, web designer, full-stack developer, or freelance web developer.
2	**Vertical:**	Open Elective
3	**Type:**	Practical
4	**Credits:**	2 credits (1 credit = 30 Hours of Practical work in a semester)
5	**Hours Allotted:**	60 hours
6	**Marks Allotted:**	50 Marks
7	**Course Objectives (CO):** **CO 1.** Familiarize students with core web development technologies such as HTML, CSS, JavaScript, and PHP. **CO 2.** Develop students' skills in creating structured and visually appealing web pages using HTML and CSS. **CO 3.** Enable students to add interactivity and dynamic behavior to web pages using JavaScript. **CO 4.** Introduce students to basic web design principles and best practices. **CO 5.** Provide practical experience through hands-on exercises and projects.	
8	**Course Outcomes (OC):** After successful completion of this course, students would be able to - **OC 1.** Demonstrate proficiency in HTML markup and CSS styling to create well-structured and visually appealing web pages. **OC 2.** Implement interactivity and dynamic behavior on web pages using JavaScript and PHP. **OC 3.** Apply basic web design principles to create user-friendly and aesthetically pleasing websites. **OC 4.** Analyze and solve problems related to web development, including troubleshooting code and debugging errors. **OC 5.** Develop basic web applications and prototypes using HTML, CSS, JavaScript, and PHP.	

	OC 6. Collaborate effectively in a team environment on web development projects.
9	**Modules:** **Module 1: Basics of Web Development (HTML and CSS) (30 hours)**
	Understanding the Internet and World Wide Web: Introduction to the Internet and its applications. Overview of email, Telnet, FTP, e-commerce, and e-business. Basics of Internet infrastructure: ISPs, DNS, URLs, and HTTP. **HTML5 Fundamentals:** Basic Elements of HTML: Introduction to HTML tags for creating the structure of web pages. Formatting Text: Applying basic text formatting using HTML tags. Organizing Content: Using lists and headings to organize content. Creating Links: Making hyperlinks to connect web pages. HTML Tables: Structuring data using HTML tables for better presentation and organization. **Working with Multimedia and Forms:** Adding Images: Inserting images onto web pages and understanding image formats. Colors and Styling: Applying colors and basic styles to web elements. Forms and User Input: Creating interactive forms for user input and data submission. **Styling with CSS** Introduction to CSS: Understanding the role of Cascading Style Sheets in styling web pages. Selectors and Properties: Using CSS selectors and properties to style HTML elements. Background and Fonts: Applying background styles and working with fonts. Positioning Elements: Understanding CSS properties for positioning elements on a web page.
	Module 2: JavaScript and Dynamic Web Content (30 hours)
	JavaScript: Integrating JavaScript: Using JavaScript code within HTML documents for interactivity. Programming Basics: Understanding JavaScript variables, operators, and control flow. Functions and Events: Defining functions and handling events for user interaction. Working with Forms: Validating form data and handling user input with JavaScript.

	Dynamic content with PHP Basics of Server-side Scripting: Understanding the role of PHP in server-side scripting. Variables and Data Types: Declaring variables, working with data types, and type coercion in PHP. Control Structures: Implementing conditional statements and loops in PHP scripts. Sessions and Cookies: Introduction to managing user sessions and using cookies for data storage. Working with Databases: Connecting to databases, executing SQL queries, and processing query results.	
10	**Text Books** 1. HTML 5 Black Book, Covers CSS 3, JavaScript, XML, XHTML, AJAX, PHP and jQuery, 2ed, Dreamtech Press, 2016 2. Web Programming and Interactive Technologies, scriptDemics, StarEdu Solutions India, 2018 3. PHP: A Beginners Guide, Vikram Vaswani, TMH	
11	**Reference Books** 1. HTML, XHTML, and CSS Bible Fifth Edition, Steven M. Schafer, WILEY, 2011 2. Learning PHP, MySQL, JavaScript, CSS & HTML5, Robin Nixon, O'Reilly, 2018 3. PHP, MySQL, JavaScript & HTML5 All-in-one for Dummies, Steve Suehring, Janet Valade Wiley, 2018	
12	**Internal Continuous Assessment: 40%**	**Semester End Examination: 60%**
13	The internal evaluation will be determined by the completion of practical tasks and the submission of corresponding write-ups for each session. Each practical exercise holds a maximum value of 10 marks. The total evaluation, out of 100 marks, should be scaled down to a final score of 20 marks. **Total: 20 marks**	A **Semester End Practical Examination** of **2 hours duration** for **30 marks** as per the paper pattern given below. **Certified Journal** is **compulsory** for appearing at the time of Practical Exam **Total: 30 Marks**
14	**Format of Question Paper:** **Total Marks: 30** **Duration: 2 Hours**	

Question	Practical Question Based On	Marks
Q. 1	Module 1	12
Q. 2	Module 2	12
Q. 3	Viva	06

OPEN ELECTIVE COURSES

Year	Sem.	Course Code	Course Title	No of Credits	No of Lectures Hours	Total Credits
1	I	OE1	Open Source Technologies	2	30	4
		OE2	Web Designing	2	30	
	II	OE3	Social Media Marketing	2	30	4
		OE4	Multimedia & Design	2	30	
II	III	OE5	Cyber & Digital Safety	2	30	2
	IV	OE6	Data Analytics	2	30	2

Sign of the BOS Chairman
Dr. Jyotshna Dongardive
Ad-hoc BOS (Computer Science)

Sign of the Offg. Associate Dean
Dr. Madhav R. Rajwade
Faculty of Science & Technology

Sign of Offg. Dean
Prof. Shivram S. Garje
Faculty of Science & Technology